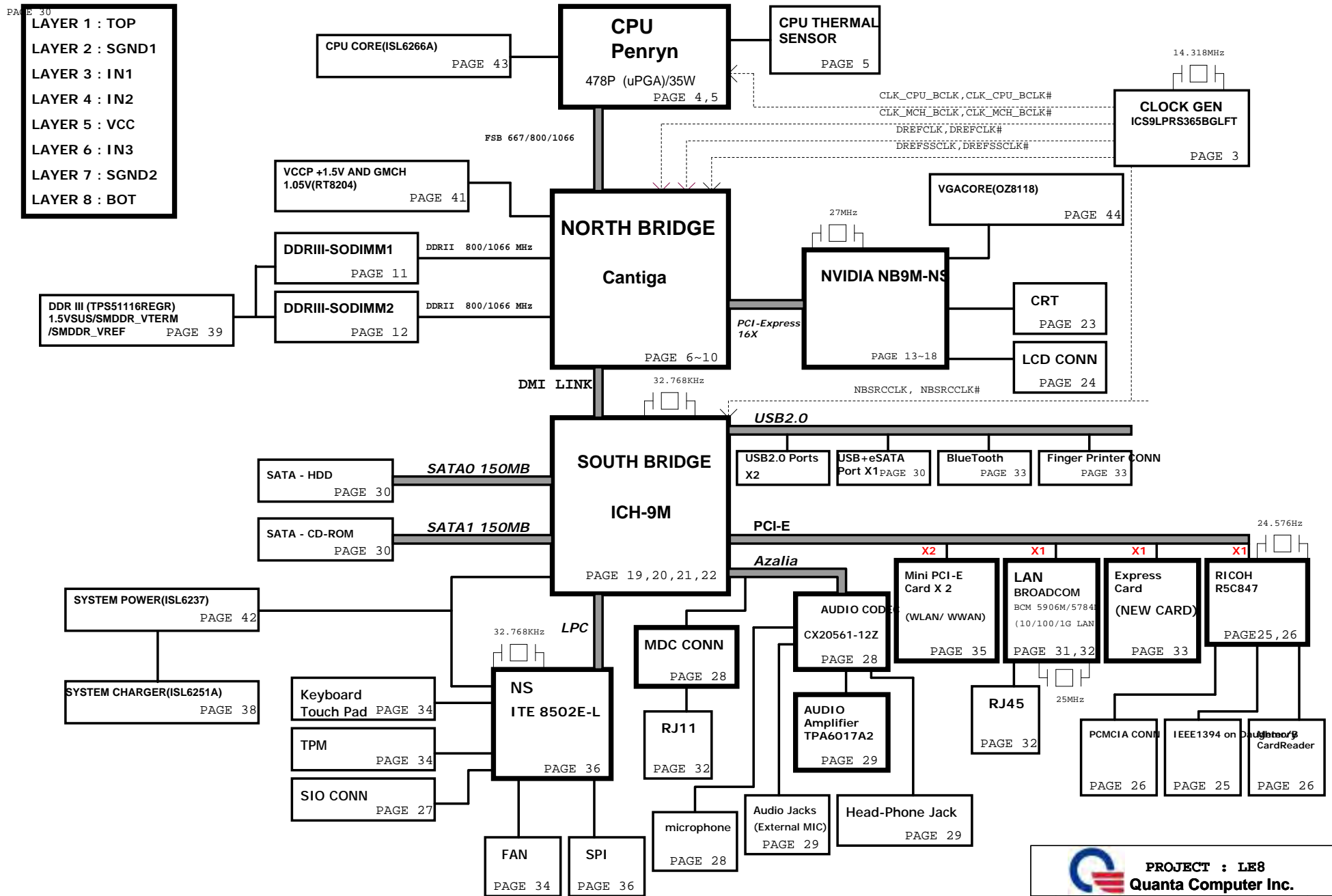


PCB STACK UP
8L

LE8(K43) BLOCK DIAGRAM

01

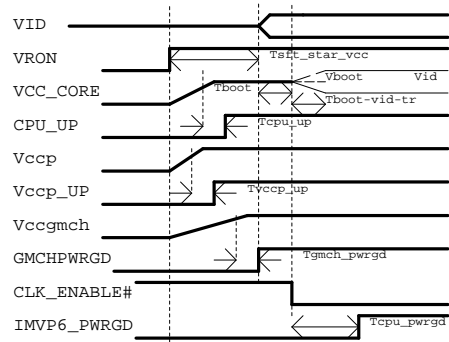


Board Stack up Description

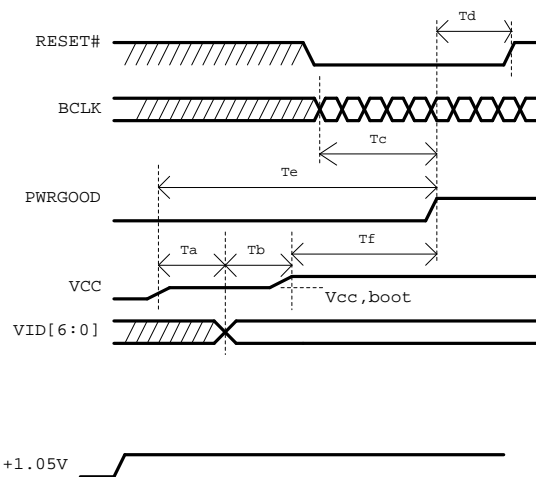
PCB Layers

Layer 1		TOP
Layer 2		GND
Layer 3		IN1
Layer 4		IN2
Layer 5		SVCC
Layer 6		IN3
Layer 7		GND
Layer 8		BOTTOM

Power On Sequencing Timing Diagram



MEROM Power-up Timing Specifications

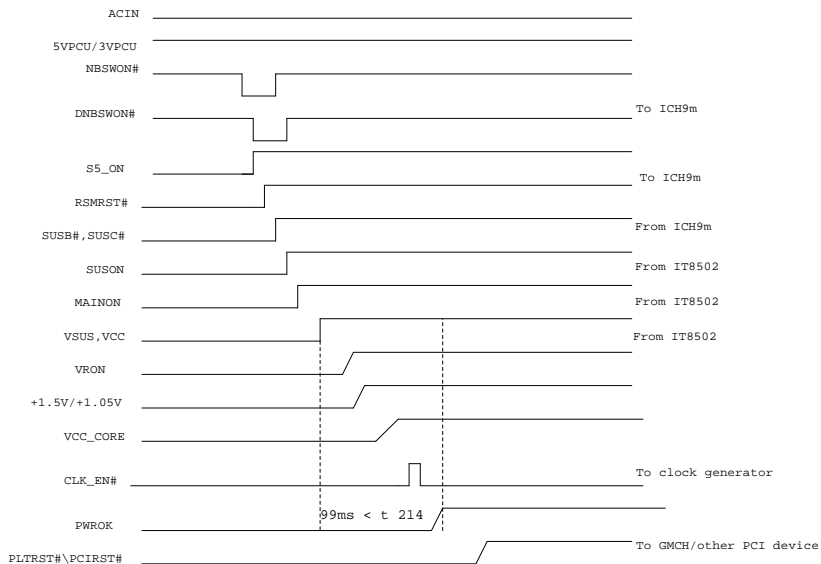


Ta=VCC and VCCP assertion to VID[6:0] valid
 Tb=VID[6:0] stable to VCC valid
 Tc=BCLK stable to PWRGOOD assertion
 Td=PWRGOOD to RESET# de-assertion time
 Te=Vcc,boot valid to PWRGOOD assertion time

Voltage Rails

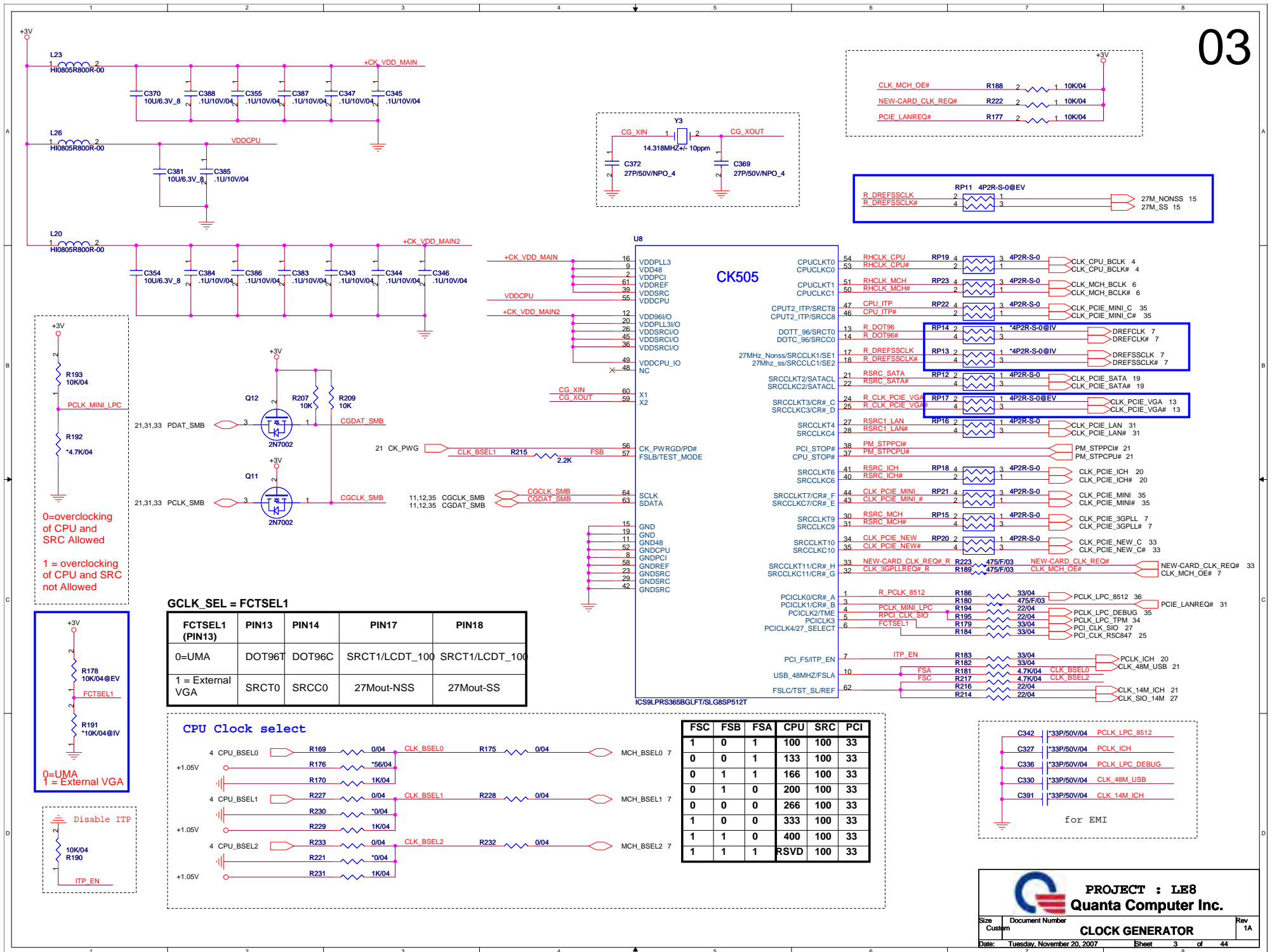
Voltage Rails	ON S0~S2	ON S3	ON S4	ON S5	Control signal
VCC_CORE	X				VRON
+1.5V	X				MAINON
+1.05V	X				MAINON
5V_S5/3V_S5	X	X	X	X	S5_ON
5VSUS/3VSUS/1.5VSUS	X	X			SUSON
SMDRR_VTERM/+3V/+5V/+15V/+1.8V	X				MAINON
+VGACORE/+VGA1.1V	X				MAINON
LANVCC	X	X	X	X	LAN_ON
3VPCU	X	X	X	X	VL
5VPCU	X	X	X	X	VL

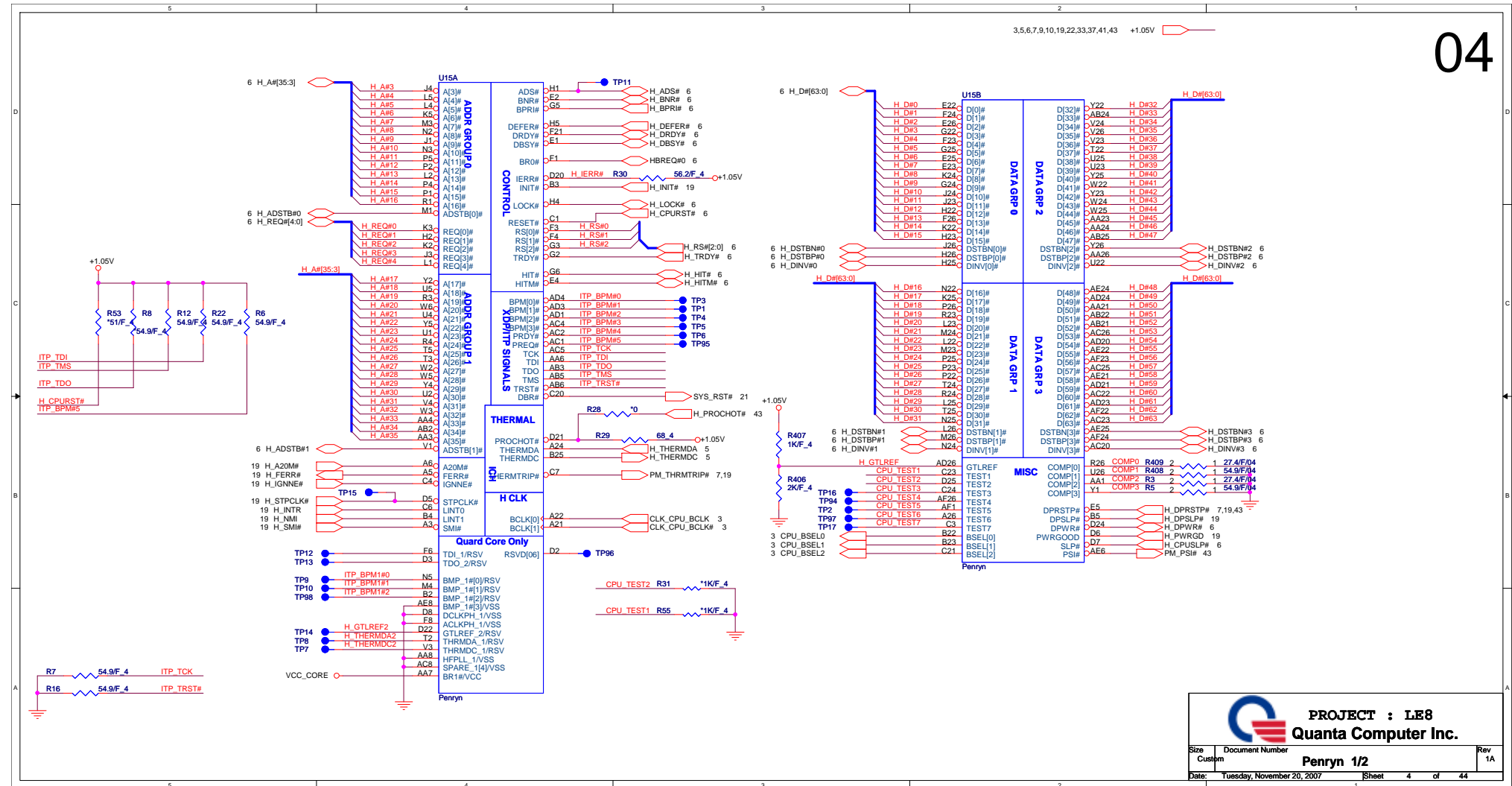
ACIN POWER ON TIMING

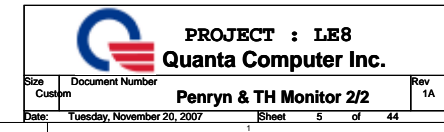


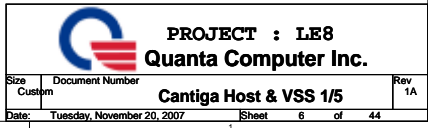
02

		PROJECT : LE8	
		Quanta Computer Inc.	
Size Custom	Document Number	SYSTEM INFORMATION	
Date: Tuesday, November 20, 2007	Sheet 2 of 44	Rev 1A	









3.5,10,11,12,13,15,16,19,20,21,22,23,24,25,26,27,28,30,31,33,34,36,37,38,40,41,42,43,44
+3V
8.10,11,12,33,37,39,44
1.5VSUS
3.4,5,6,9,10,19,22,33,37,41,43
+1.05V_PEG
10

07

MCH_CFG_5 DMIX2 selection

Low: DMIX2
High: DMIX4 (Default)

MCH_CFG_16 FSB Dynamic ODT

Low: Dynamic ODT disabled
High: Dynamic ODT enabled (Default)

MCH_CFG_9 PCI Express Graphic Lane

Low: Reverse Lane
High: Normal operation(Default)

MCH_CFG_19 DMI Lane Reversal

Low: Normal (Default)
High: Lane Reserved

MCH_CFG_6 ITPM Host Interface

Low: ITPM Host Interface enabled
High: ITPM Host Interface disabled (Default)

MCH_CFG_7 Intel (R) Management Engine Crypto

Low: Intel (R) Management Engine Crypto
TLS cipher suite with no confidentiality

High: Intel (R) Management Engine Crypto
TLS cipher suite with no confidentiality (Default)

MCH_CFG_10 PCIe Lookback Enable

Low: Enabled
High: Disabled (Default)

MCH_CFG_12/13 XOR/ALLZ/CLOCK Un-gating

MCH_CFG_13 MCH_CFG_12 Configuration

0 0 Reserved
0 0 XOR Mode enabled
1 0 All-Z Mode enabled
1 1 Normal operation (Default)

TP30 AL34 ME_JTAG_TCK
TP30 AN35 ME_JTAG_TDO
TP30 AM35 ME_JTAG_TMS

3 MCH_BSEL0
3 MCH_BSEL1
3 MCH_BSEL2

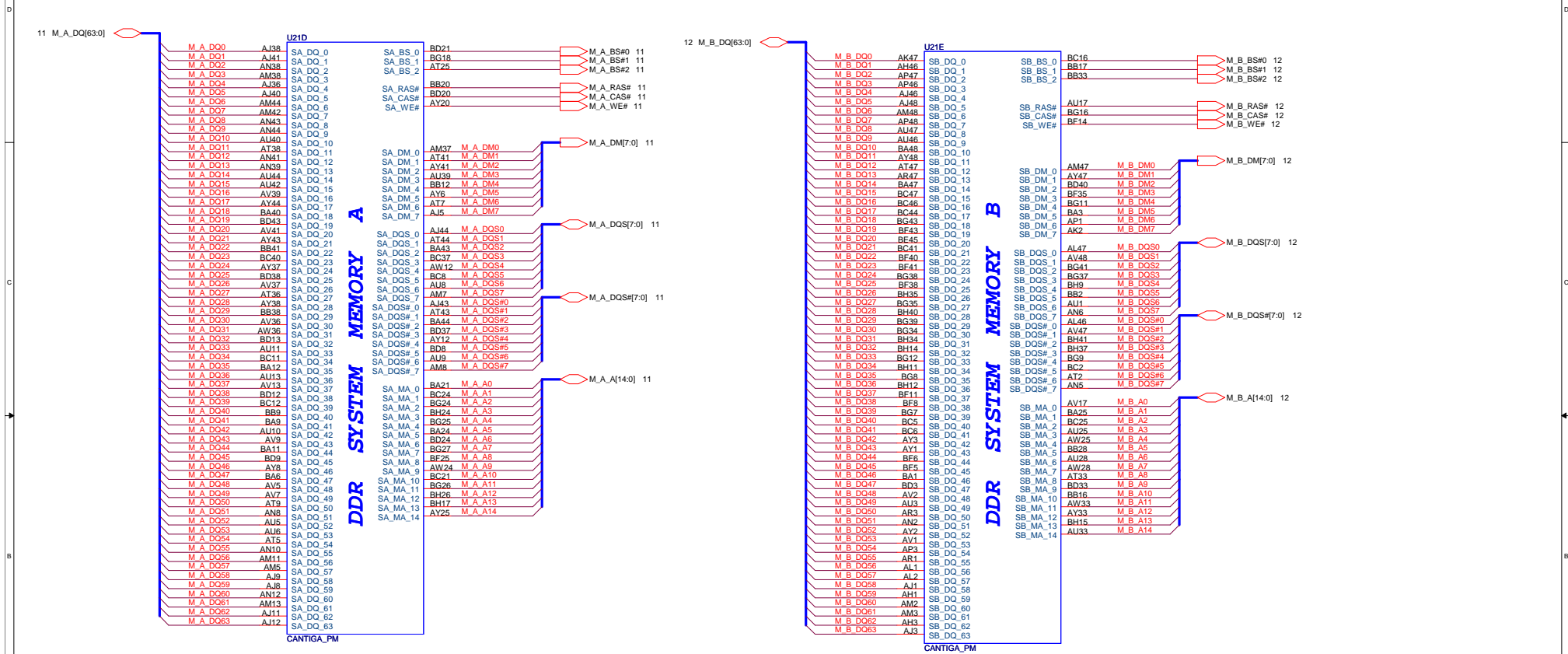
TP25 MCH_CFG_3
TP27 MCH_CFG_4
TP28 MCH_CFG_5
TP29 MCH_CFG_6
TP30 MCH_CFG_7
TP31 MCH_CFG_8
TP32 MCH_CFG_9
TP33 MCH_CFG_10
TP34 MCH_CFG_11
TP35 MCH_CFG_12
TP36 MCH_CFG_13
TP37 MCH_CFG_14
TP38 MCH_CFG_15
TP39 MCH_CFG_16
TP40 MCH_CFG_17
TP41 MCH_CFG_18
TP42 MCH_CFG_19
TP43 MCH_CFG_20

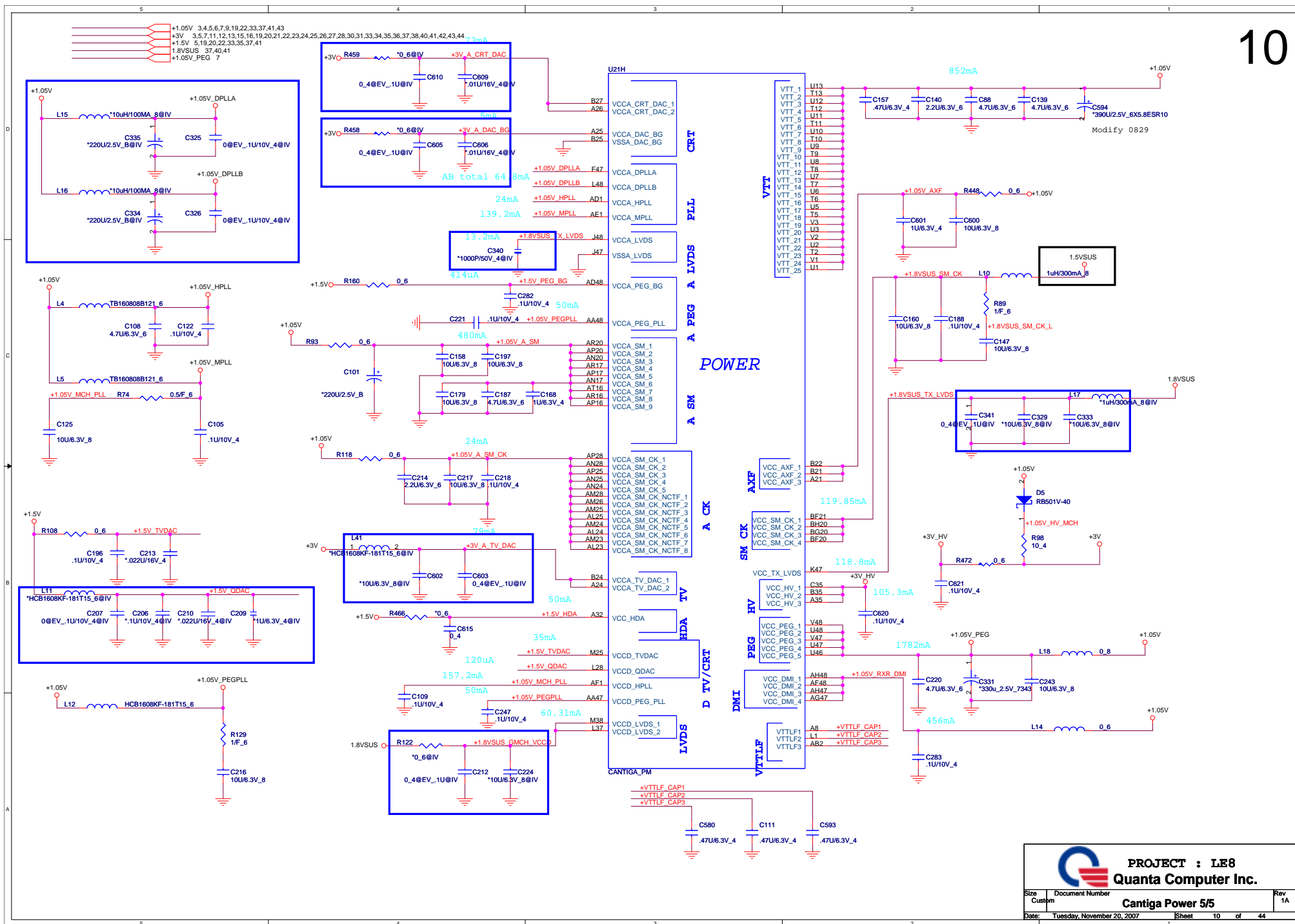
21 PM_SYNC#
4,19,43 H_DPRSTP#
11,12 PM_EXTS#0
PM_EXTS#0
PM_EXTS#1
PM_EXTS#1
21,43 DELAY_VR_PWRGOOD
13,20 PLT_RST-R#
419 PM_THRMTRIP#
21,43 DPRSTPVR

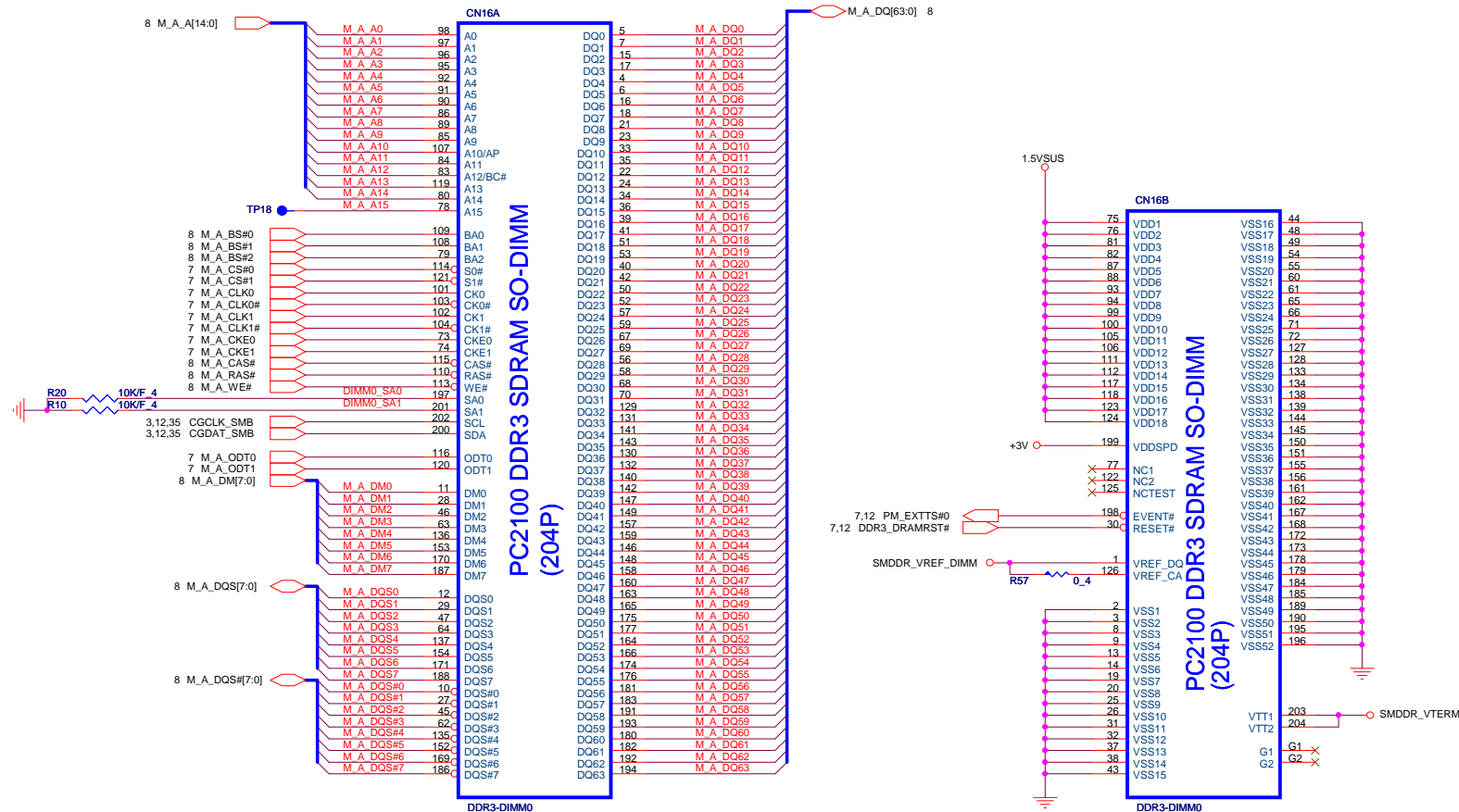
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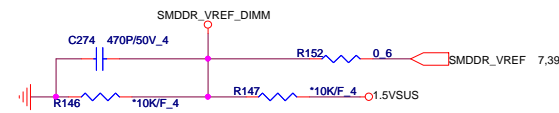
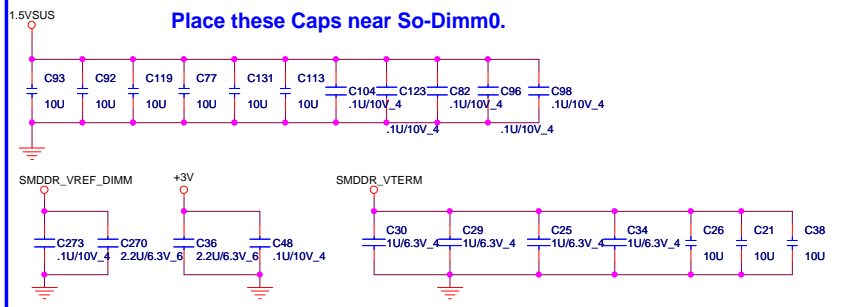
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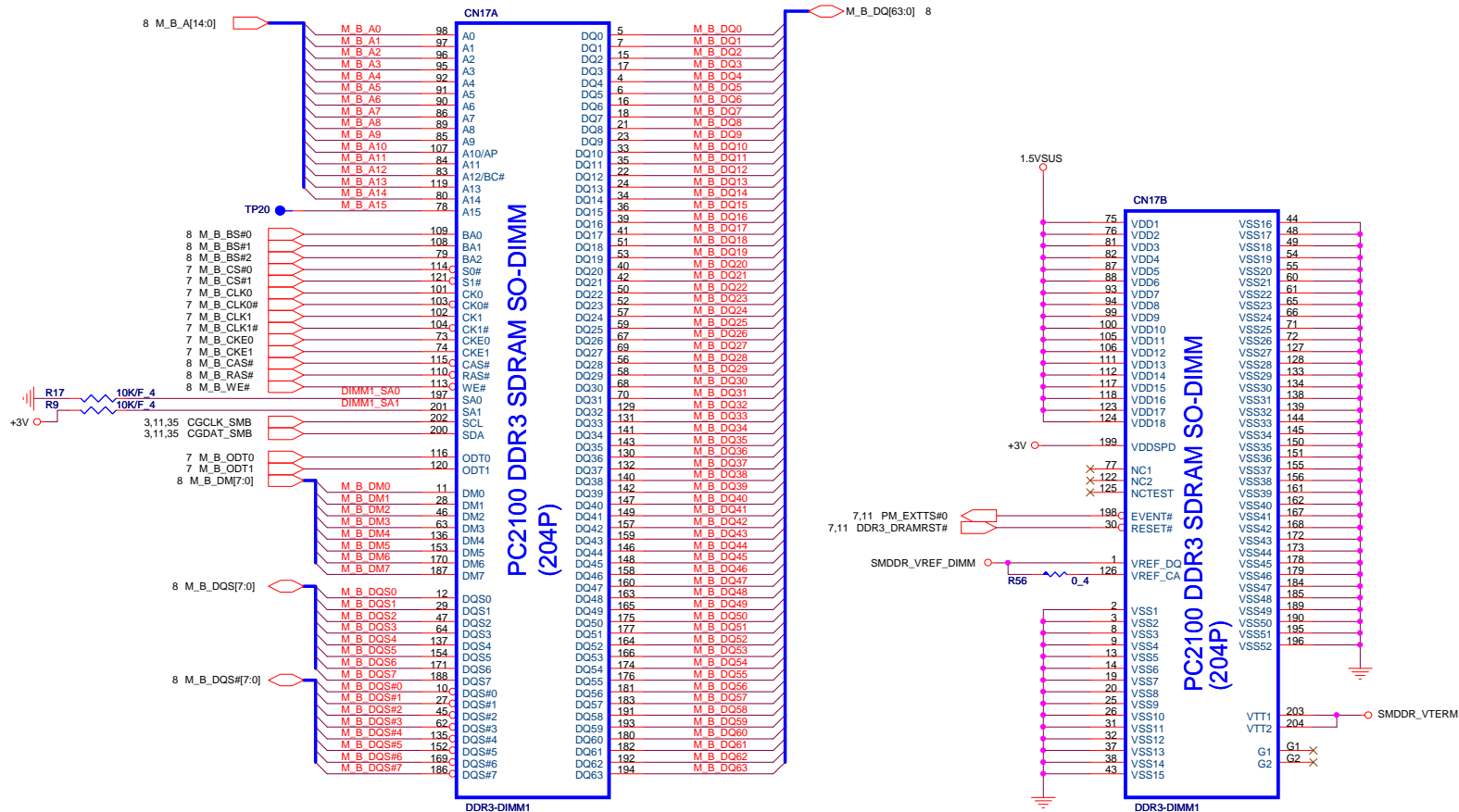


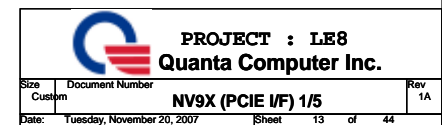


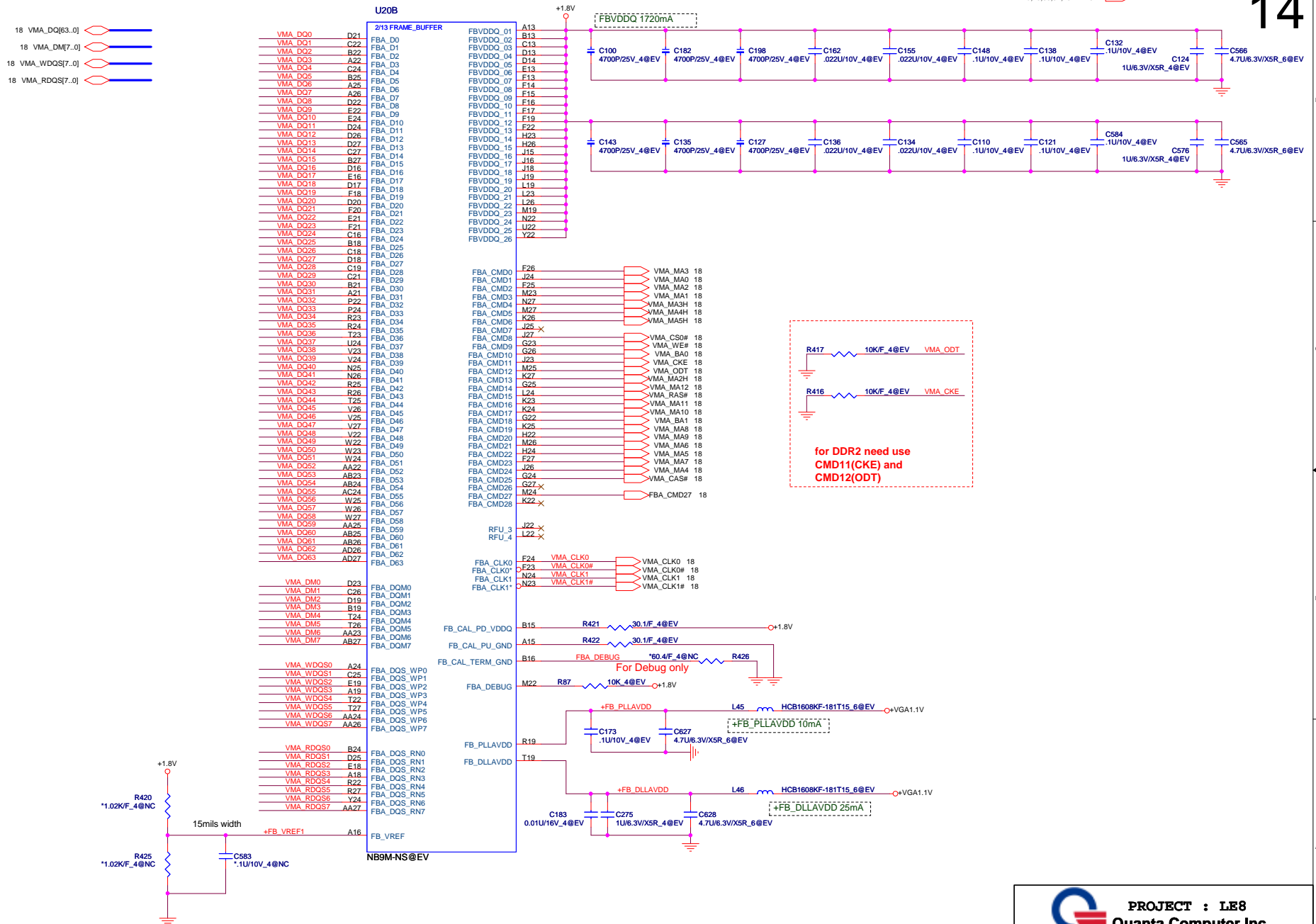


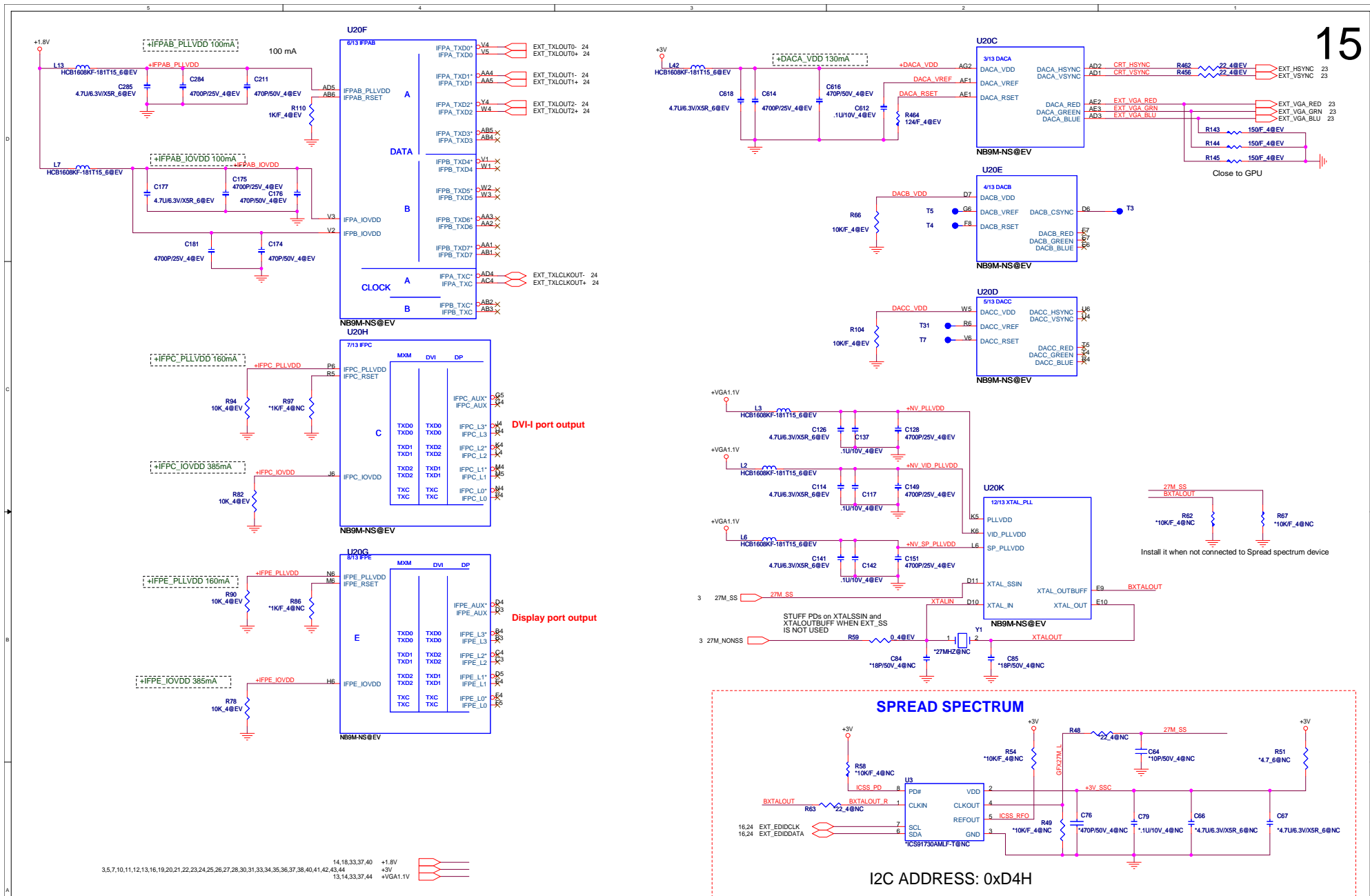
Place these Caps near So-Dimm0.

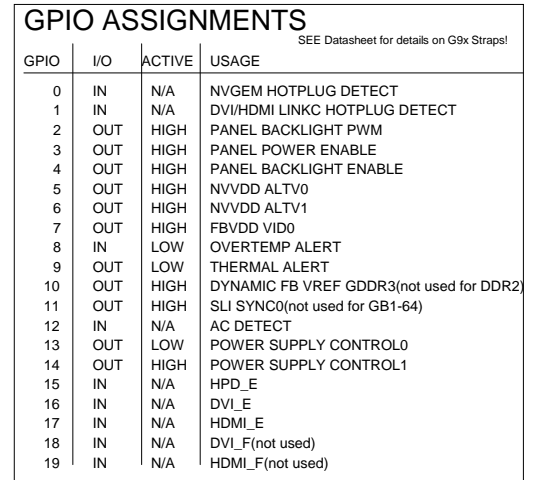












STRAP	VALUE		MEMORY Vendor
0	5K	PD	NOT USED
1	10K	PD	Samsung DDR2 16Mx16
2	15K	PD	Qmnd DDR2 16Mx16
3	20K	PD	HYNX DDR2 16Mx16
4	25K	PD	NOT USED
5	30K	PD	Samsung DDR2 32Mx16
6	35K	PD	Qmnd DDR2 32Mx16
7	45K	PD	HYNX DDR2 32Mx16

U20J

13/13 GND_NC

AC11	GND_01	NC_01	AA6
AC14	GND_02	NC_02	AC19
AC17	GND_03	NC_03	E15
AC2	GND_04	NC_04	T6
AC20	GND_05		
AC23	GND_06		
AC26	GND_07		
AC5	GND_08		
AC8	GND_09		
AE11	GND_10		
AE14	GND_11		
AE17	GND_12		
AE2	GND_13		
AE20	GND_14		
AE23	GND_15		
AE26	GND_16		
AE5	GND_17		
AF8	GND_18		
B11	GND_19		
B14	GND_20		
B17	GND_21		
B2	GND_22		
B20	GND_23		
B23	GND_24		
B26	GND_25		
B5	GND_26		
B8	GND_27		
E11	GND_28		
E14	GND_29		
E17	GND_30		
E2	GND_31		
E20	GND_32		
E23	GND_33		
E26	GND_34		
E5	GND_35		
E8	GND_36		
H2	GND_37		
H5	GND_38		
J11	GND_39		
J14	GND_40		
J17	GND_41		
K19	GND_42		
K9	GND_43		
L11	GND_44		
L12	GND_45		
L13	GND_46		
L14	GND_47		
L15	GND_48		
L16	GND_49		
L17	GND_50		
L2	GND_51		
L5	GND_52		
M12	GND_53		
M13	GND_54		
M14	GND_55		
M15	GND_56		
M16	GND_57		
P19	GND_58		
P2	GND_59		
P23	GND_60		
P26	GND_61		
P5	GND_62		
P9	GND_63		
T12	GND_64		
T13	GND_65		
T14	GND_66		
T15	GND_67		
T16	GND_68		
U11	GND_69		
U12	GND_70		
U13	GND_71		
U14	GND_72		
U15	GND_73		
U16	GND_74		
U17	GND_75		
U2	GND_76		
U23	GND_77		
U26	GND_78		
U5	GND_79		
V19	GND_80		
V9	GND_81		
W11	GND_82		
W14	GND_83		
W17	GND_84		
Y2	GND_85		
Y23	GND_86		
Y26	GND_87		
Y5	GND_88		

NB9M-NS@EV

NB9M: VGACORE +0.9V ~ +1.0V

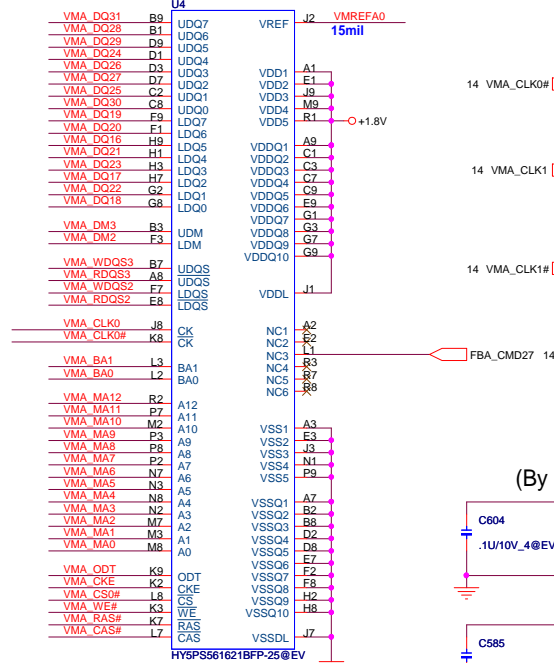
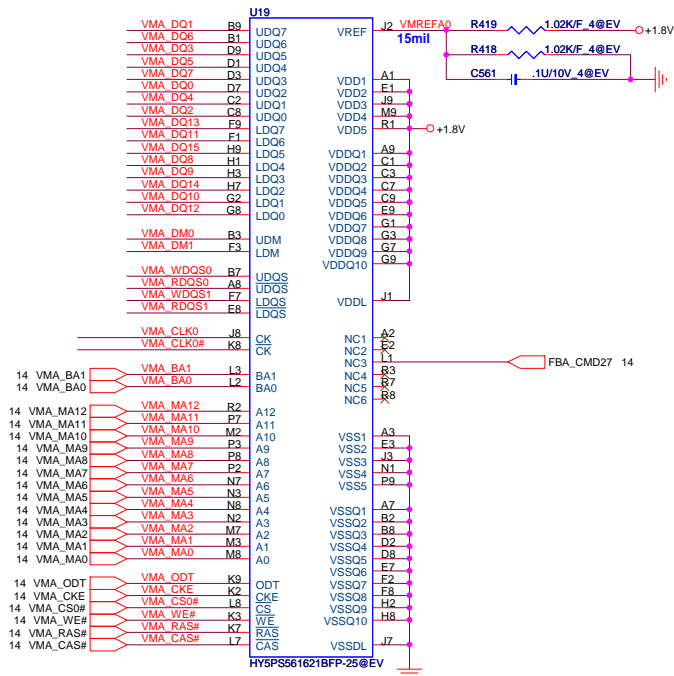
power up sequence

PXE 1.1VDD
I/O 3.3V
NVCORE
1.8VFBDDQ

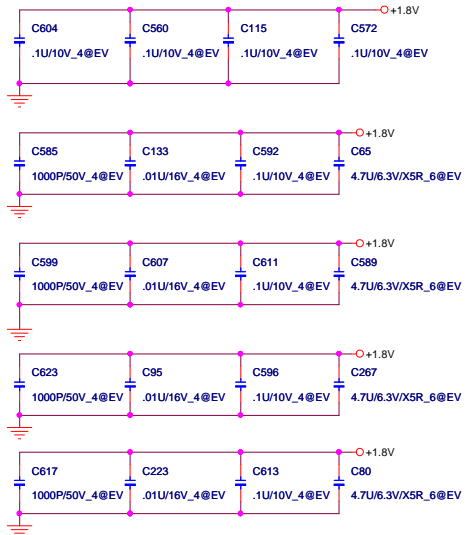


PROJECT : LE8
Quanta Computer Inc.

Size	Document Number	Rev
Custm	NV9X GND 5/5	1A
Date:	Tuesday, November 20, 2007	Sheet 17 of 44



(By pass capacitor)

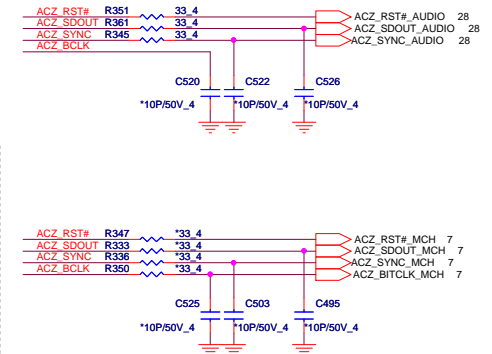
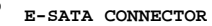


512Mb(32Mx16) : AKD5FG-TW31/Hynix(HY5PS121621CFP-25)
AKD5FG-T'04/Quimonda(HYB18T512161B2F-20)

- 14 VMA_DQ[63..0]
- 14 VMA_DM[7..0]
- 14 VMA_WDQS[7..0]
- 14 VMA_RDQS[7..0]
- 14,15,33,37,40 +1.8V

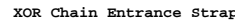
PROJECT : LE8
Quanta Computer Inc.

Size	Document Number	Rev
Custom	NV9X VRAM-1(GDDR2 BGA84)	1A
Date:	Tuesday, November 20, 2007	Sheet 18 of 44



ICH9-M Internal VR Enable strap (Internal VR for Vccsus1_05,VccSus1_5 and VccCL1_5)	ICH9-M LAN100_SLP Strap (Internal VR for VccLAN1_05 and VccCL1.05)
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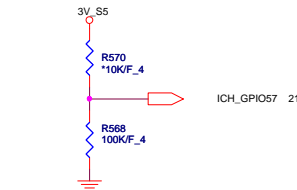
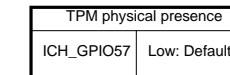
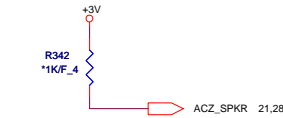
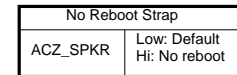
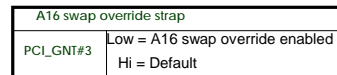
LAN100_SLP	Low = Internal VR disabled High = Internal VR enable(Default)
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ICH9 Boot BIOS select

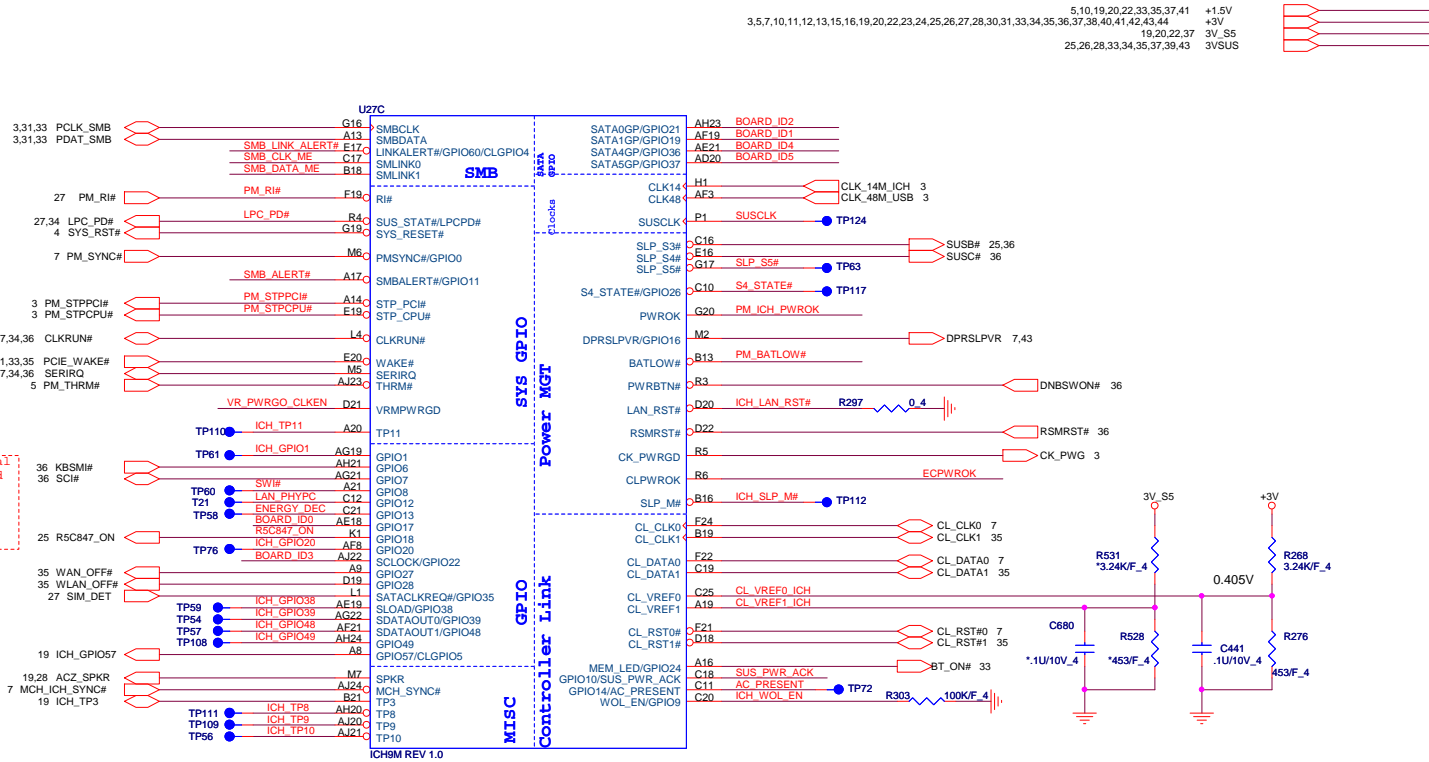
STRAP	PCI_GNT0#	SPI_CS#1
SPI	0	1
PCI	1	0
LPC	1	1

(default)



		PROJECT : LE8 Quanta Computer Inc.		
Size Custom	Document Number ICH9-M Host 1/4	Date Tuesday, November 20, 2007	Sheet 19 of 44	Rev 1A



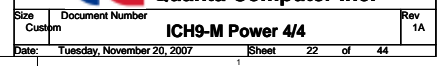


Board ID For Function	ID3 GPIO22	ID2 GPIO21	ID1 GPIO19	ID0 GPIO17
Default	0	0	0	0
	0	0	0	1
	0	0	1	0
	0	0	1	1
	0	1	0	0
	0	1	0	1
	0	1	1	0
	0	1	1	1
	1	0	0	0
	1	0	0	1
	1	0	1	0
	1	0	1	1
	1	1	0	0
	1	1	0	1
	1	1	1	0
	1	1	1	1

Board ID For Model	ID5 GPIO37	ID4 GPIO36
LE6	0	0
LE7	0	1
LE8	1	0
LE9	1	1

PROJECT : LE8
Quanta Computer Inc.

Size Document Number
Custom ICH9-M GPIO 3/4
Date: Tuesday, November 20, 2007 Sheet 21 of 44 Rev 1A

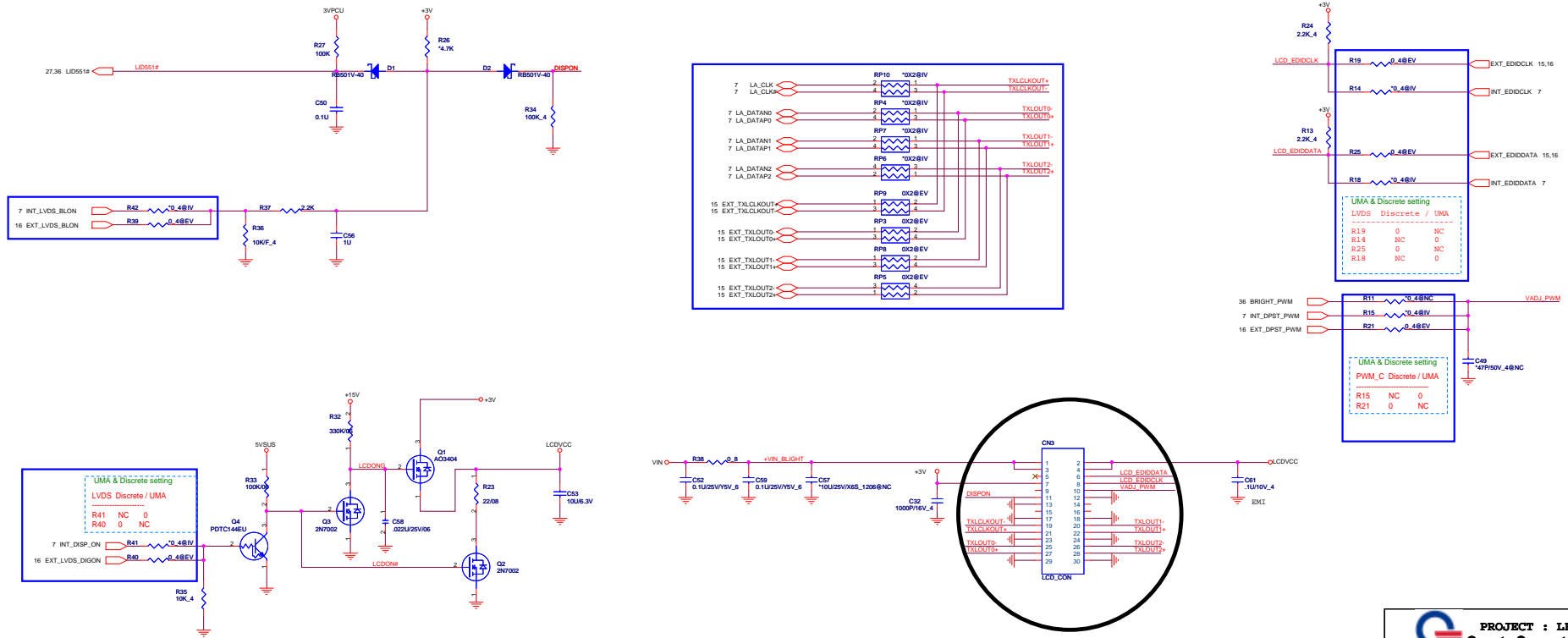


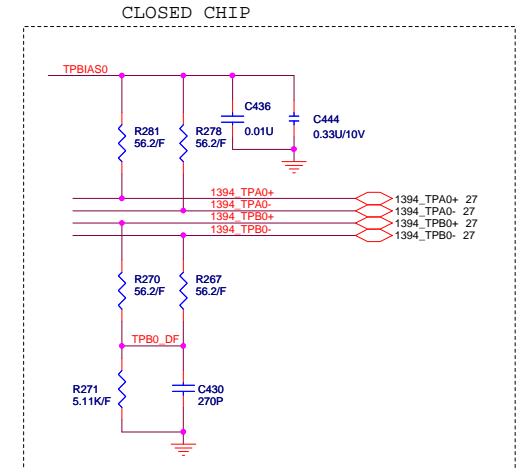
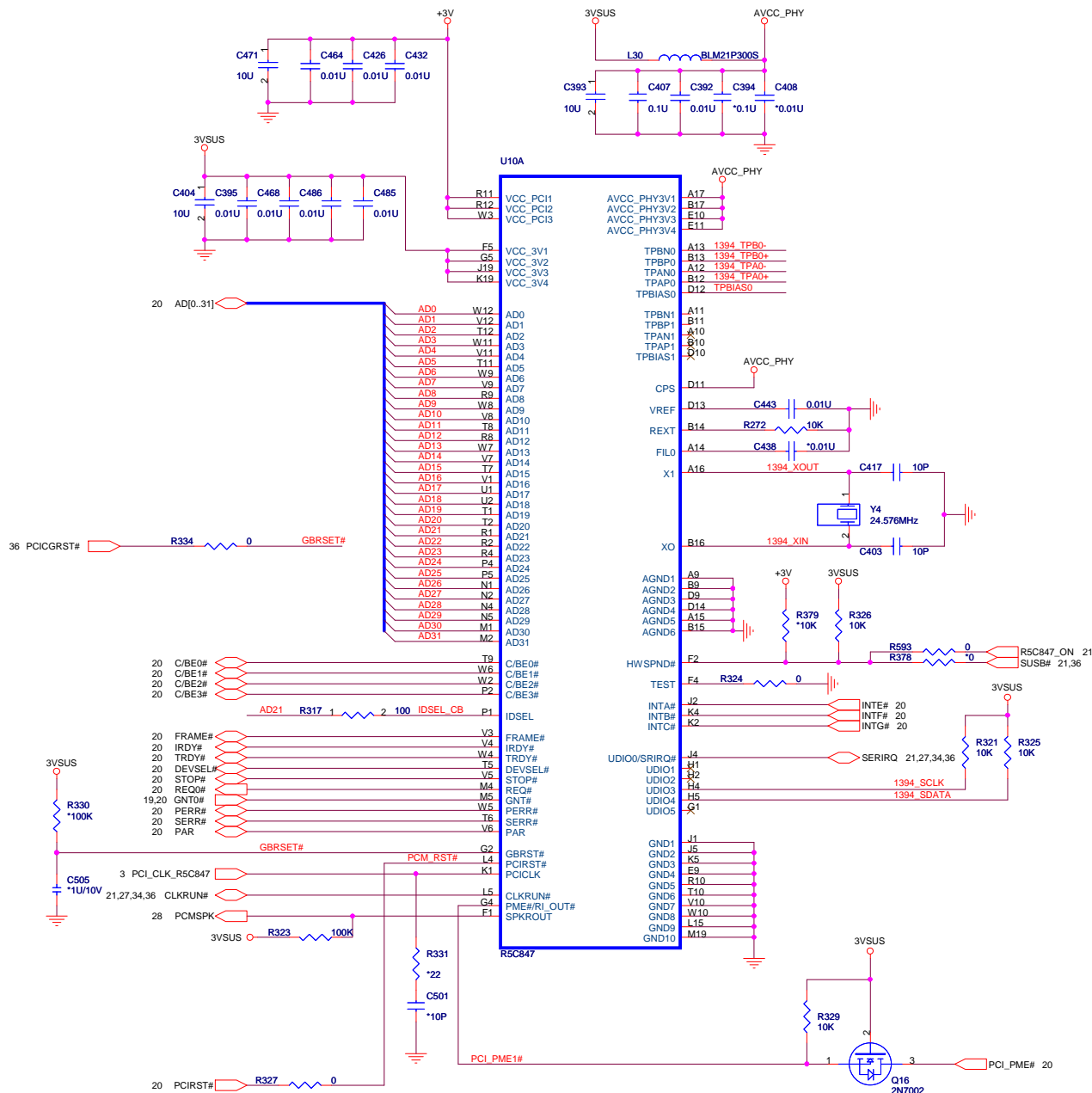


UMA & Discrete setting
LVDS Discrete / UMA


R150	0	NC
R149	0	NC
R148	0	NC
R463	0	NC
R457	0	NC
R451	0	NC
R91	0	NC
R155	NC	0
R154	NC	0
R153	NC	0
R461	NC	0
R460	NC	0
R452	NC	0
R92	NC	0

Size	Document Number	Rev
Custom	CRT CON	1A
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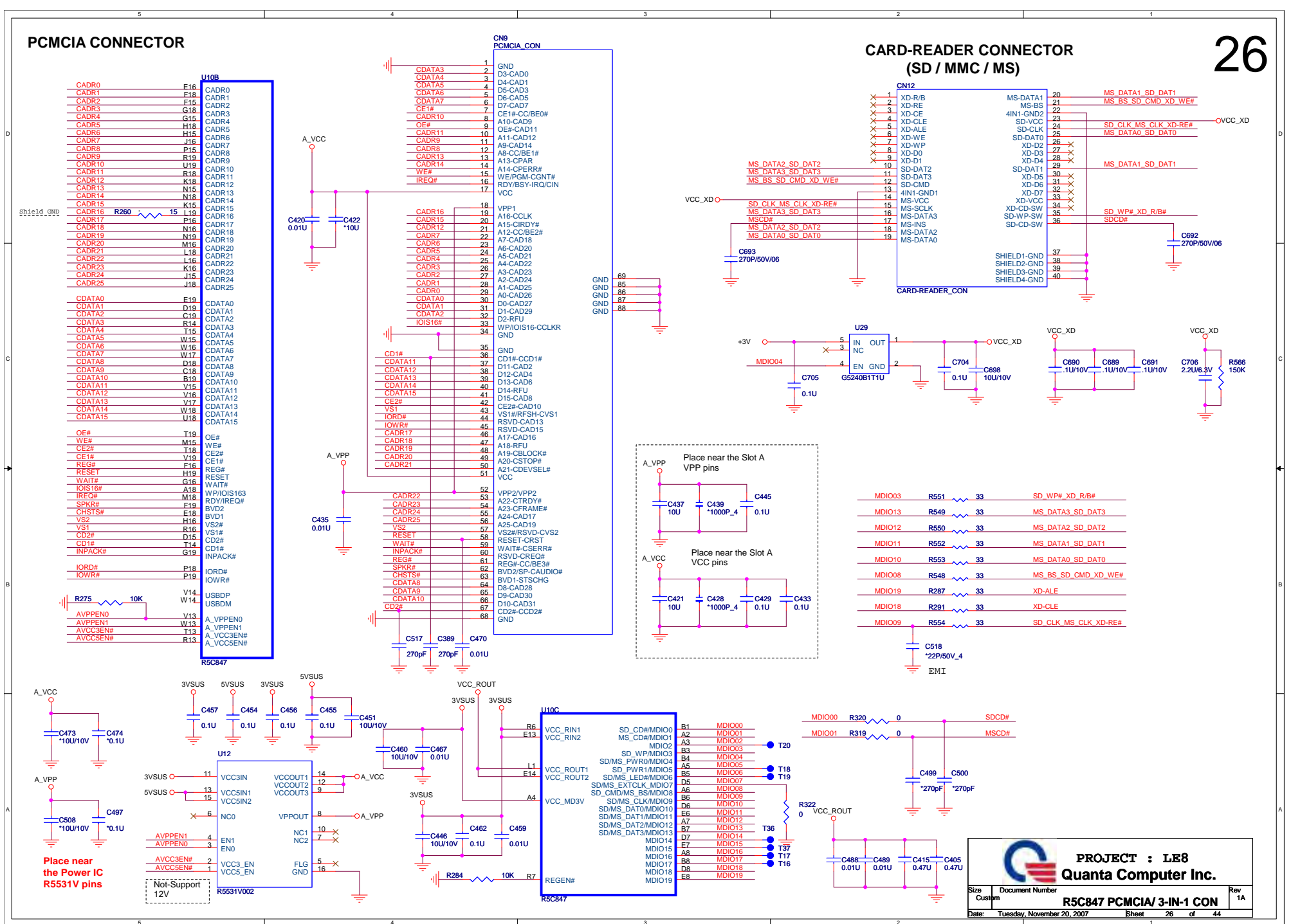
R5C847 pin to pin compatible with R5C843/R5C841

		PROJECT : LE8	
		Quanta Computer Inc.	
Size	Document Number	R5C847 PCI/1394	
Custom			
Date:	Tuesday, November 20, 2007	Sheet	25 of 44
			Rev 1A

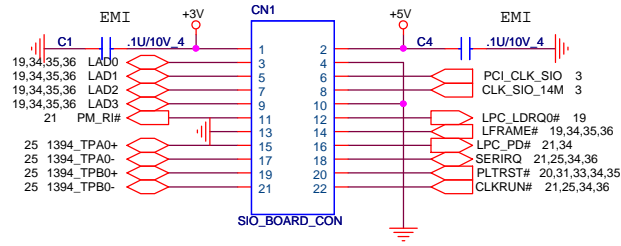
PCMCIA CONNECTOR

CARD-READER CONNECTOR (SD / MMC / MS)

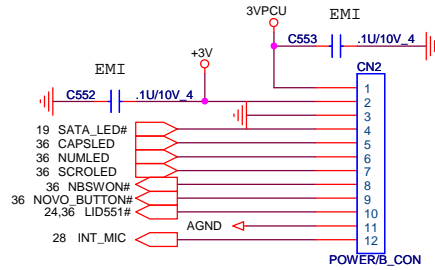
26



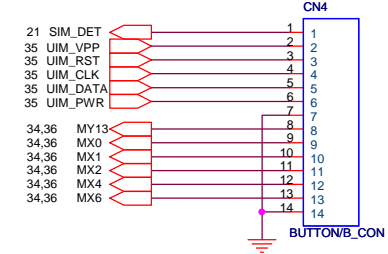
SIO BOARD



POWER BOARD

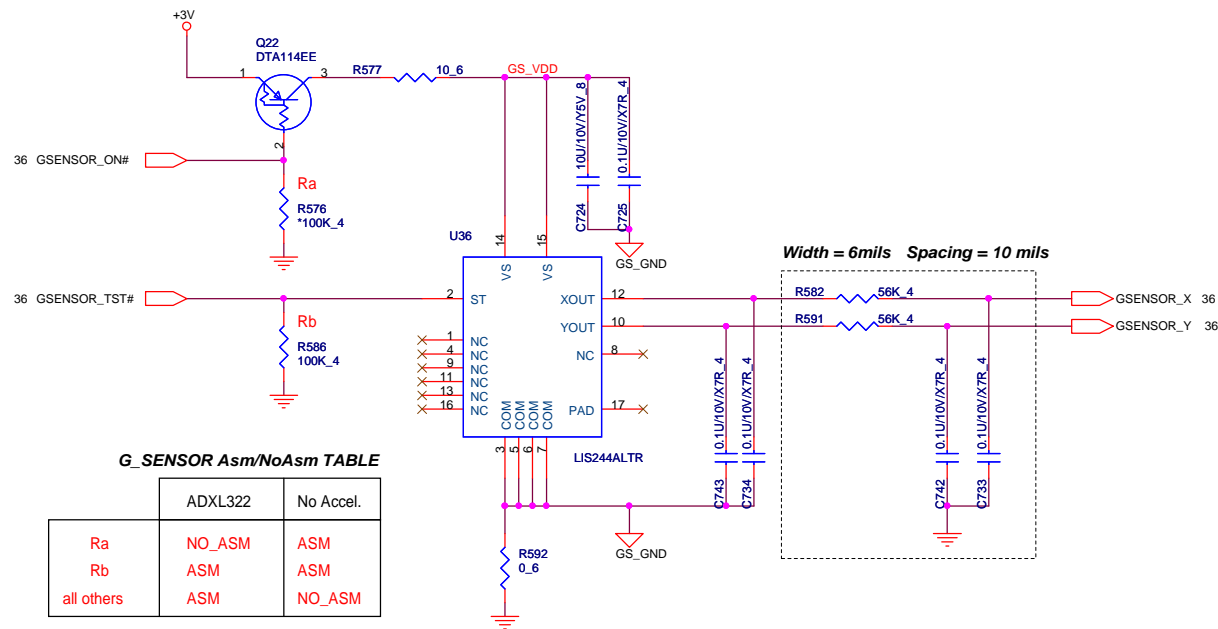


BUTTON BOARD



27

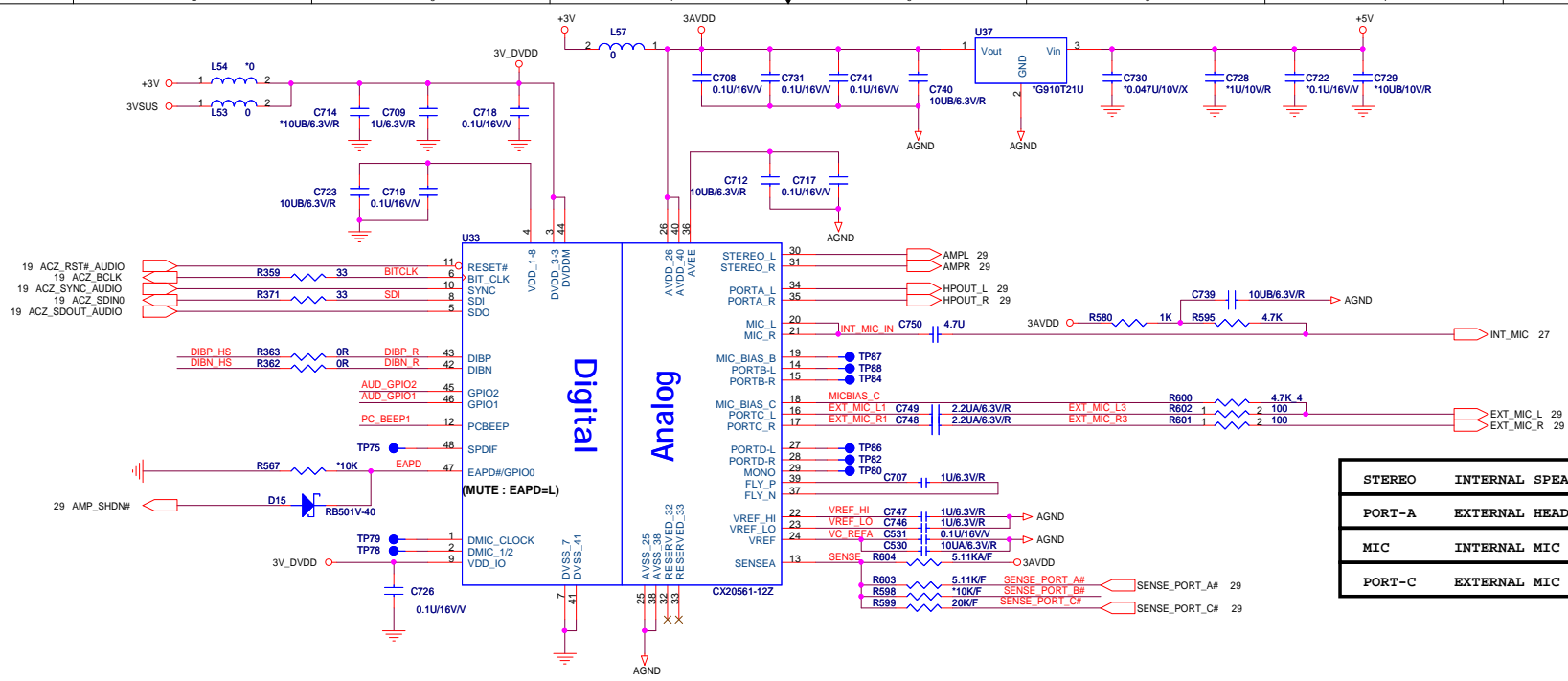
G-SENSOR



PROJECT : LE8
Quanta Computer Inc.

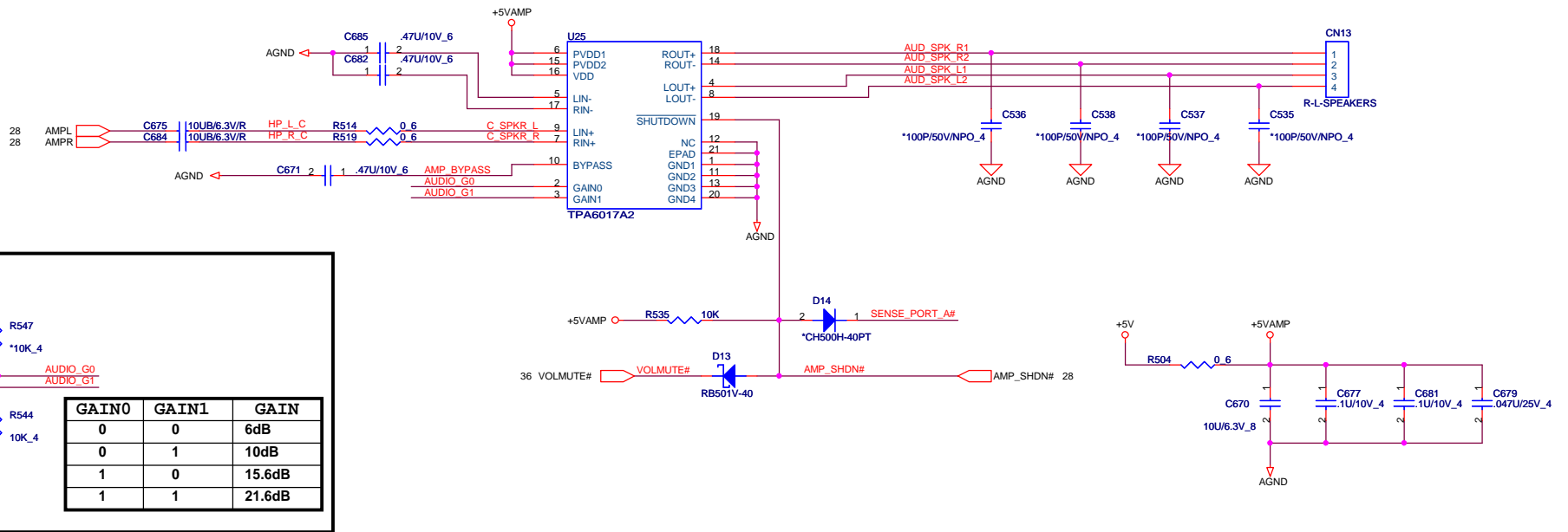
Size: Custom Document Number: T/Bs & G-SENSOR Rev: 1A

Date: Tuesday, November 20, 2007 Sheet: 27 of 44

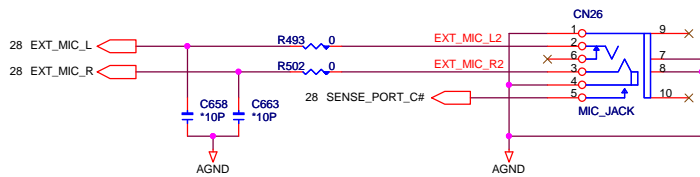


INTERNAL SPEAKER AMPLIFIER

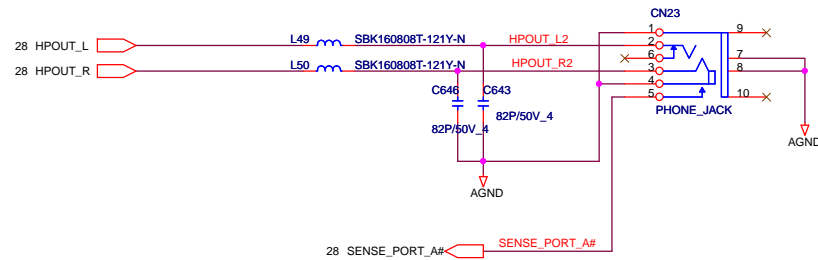
29




MIC-IN JACK



HEADPHONE

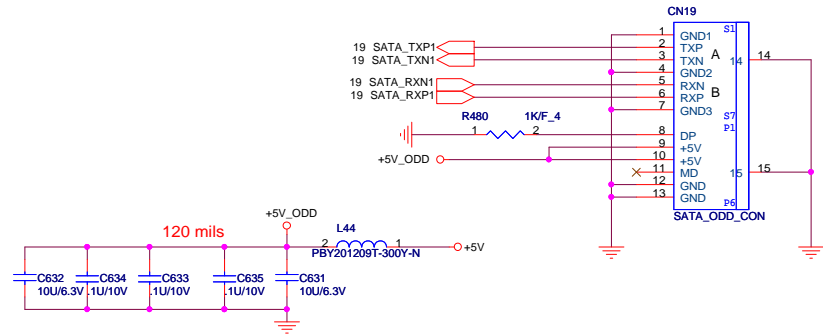




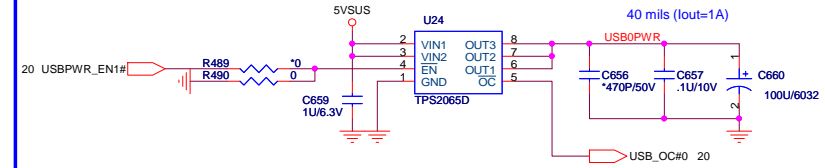
PROJECT : LE8
Quanta Computer Inc.

Size Custom	Document Number AMP_TPA6017 & JACKS	Rev 1A
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SATA CD-ROM

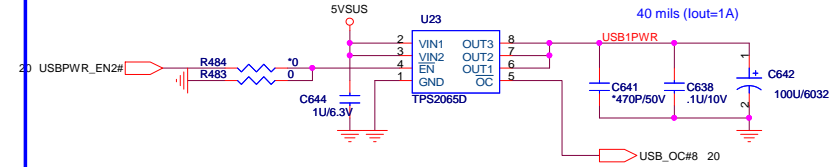
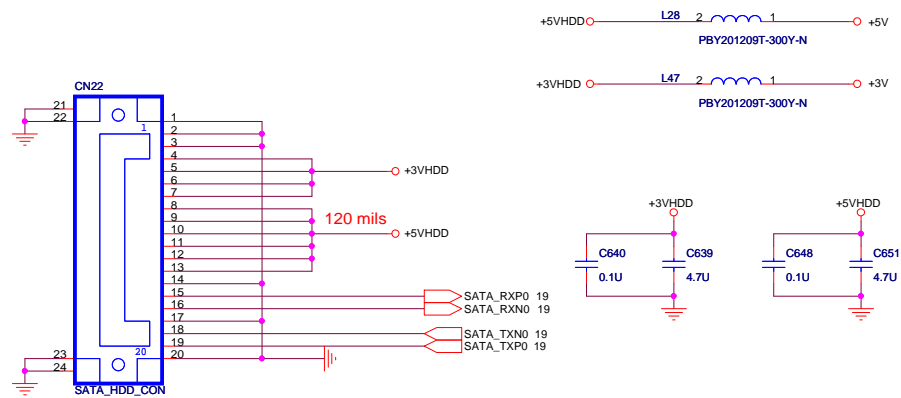


USBX2



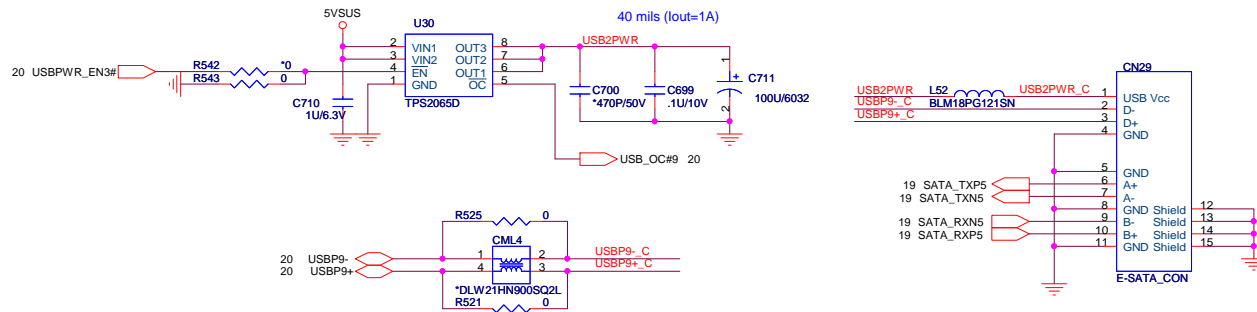
USB 0

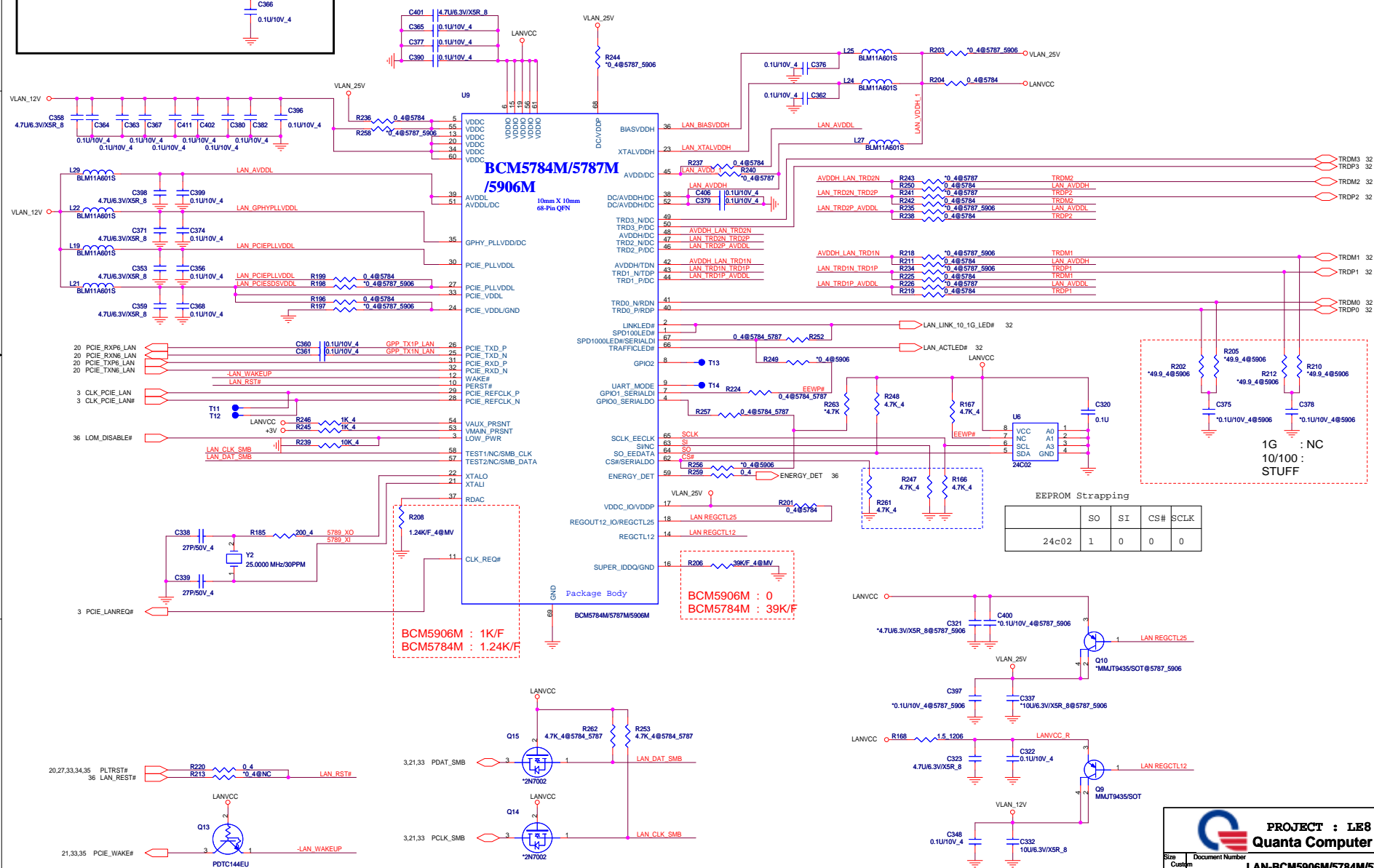
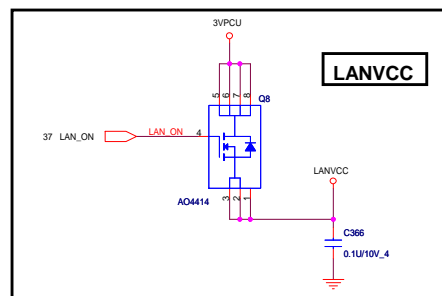
SATA-HDD CONNECTOR



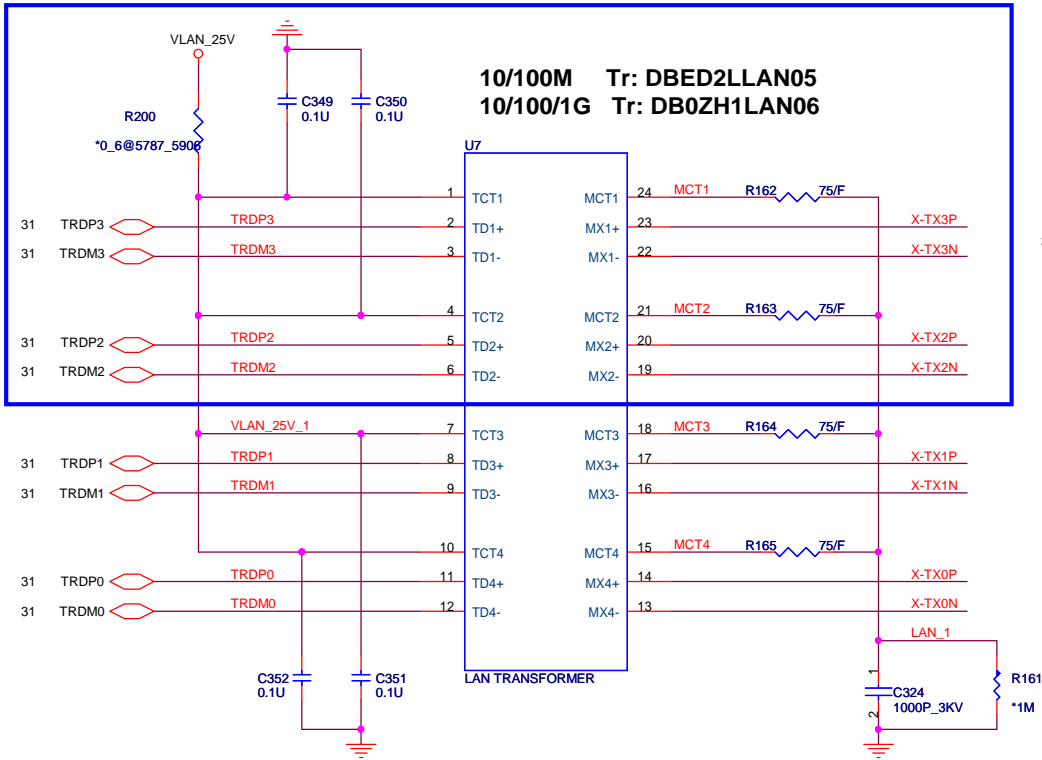
USB 1

USB + eSATA CONNECTOR

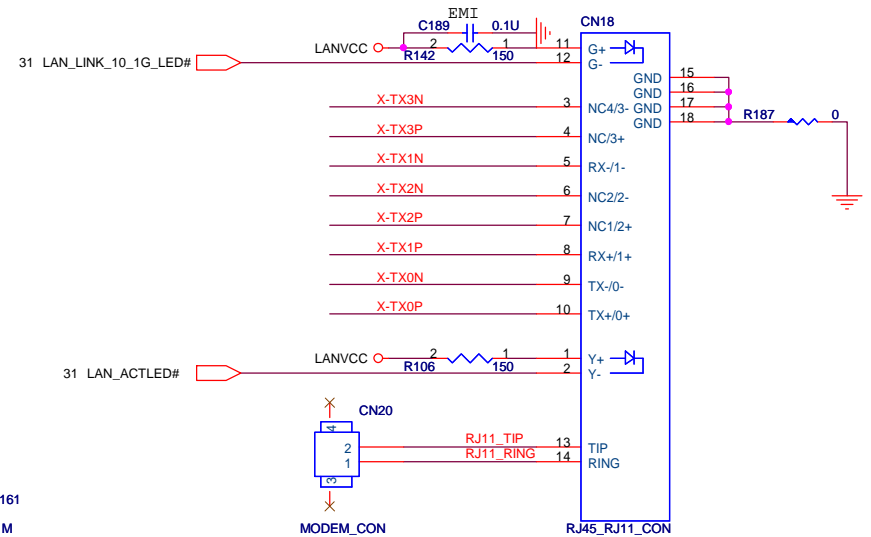





10/100M Tr: DBED2LLAN05
10/100/1G Tr: DB0ZH1LAN06

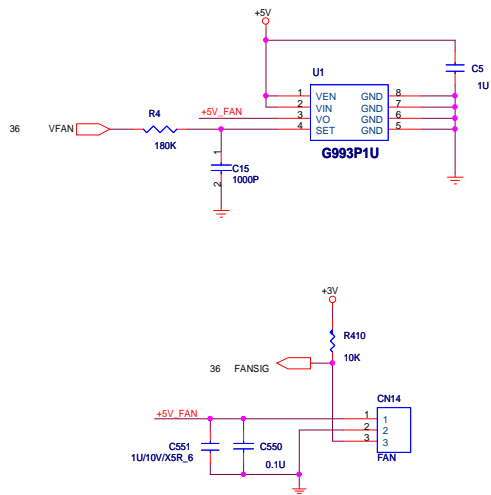


RJ45/RJ11 Connector

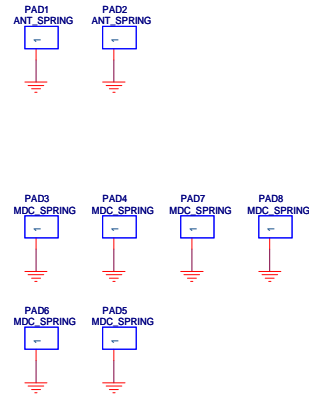


		PROJECT : LE8	
		Quanta Computer Inc.	
Size Custom	Document Number	RJ11/RJ45 CONNECTOR	
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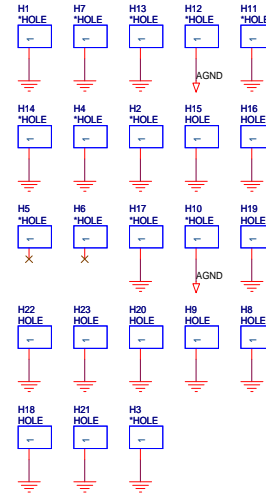
FAN CONTROL



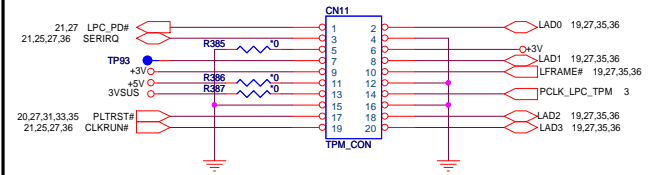
EMI PAD



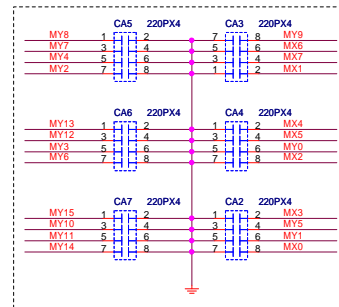
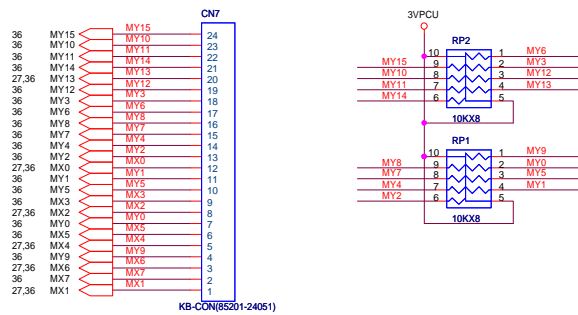
HOLES



Board TO Board TPM MODULE

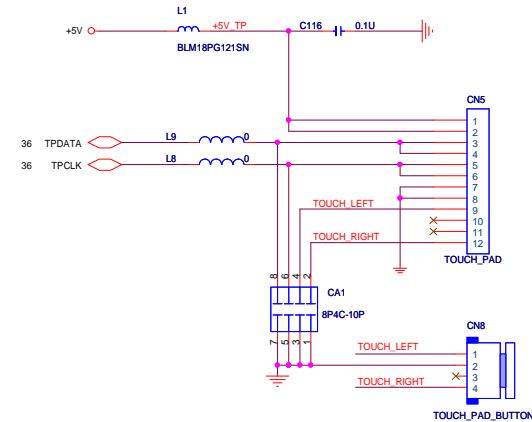



KEYBOARD



For EMI request

TOUCH PAD



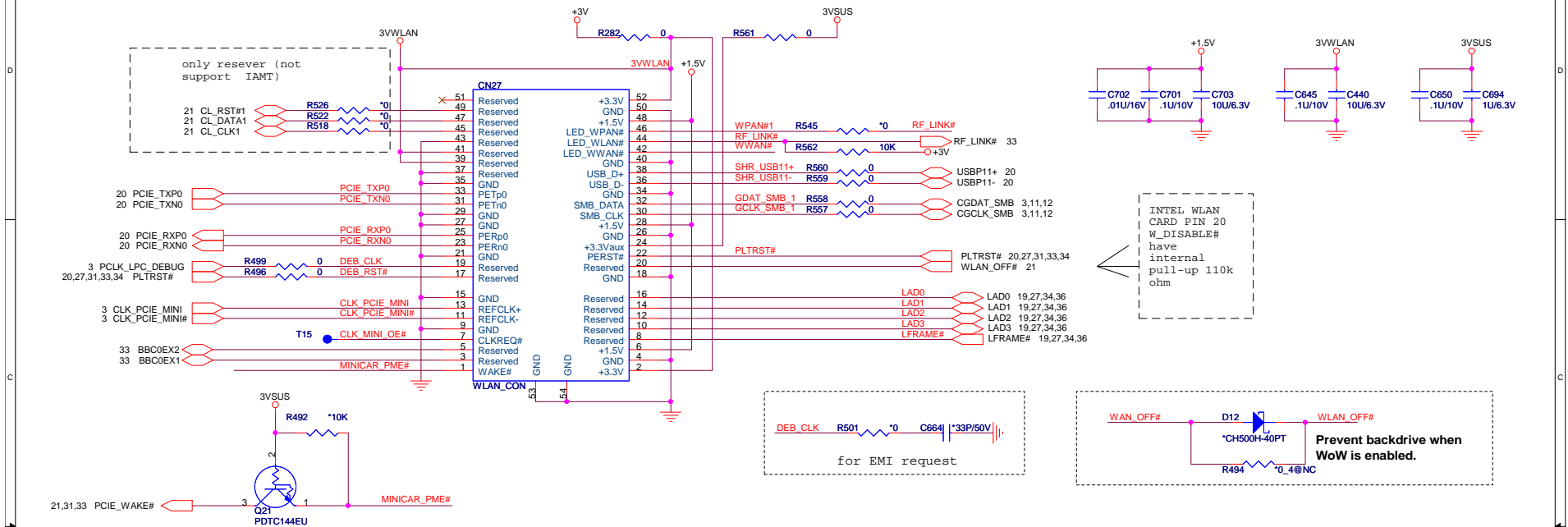


PROJECT : LB8
Quanta Computer Inc.

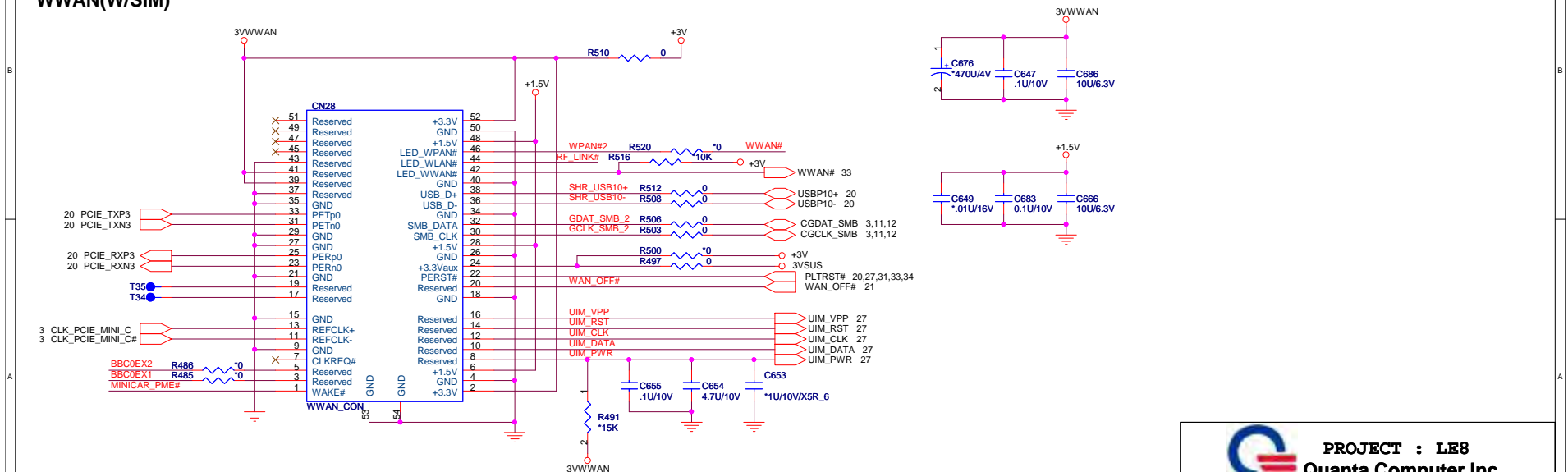
Size	Document Number	FAN, TPM, K/B, T/P	Rev 1A
Custom			
Date	Tuesday, November 20, 2007	Sheet	34 of 44

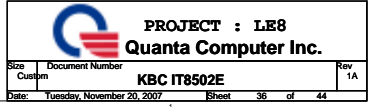
Mini PCI-E Card 1 WLAN

35



Mini PCI-E Card 2 WWAN(W/SIM)



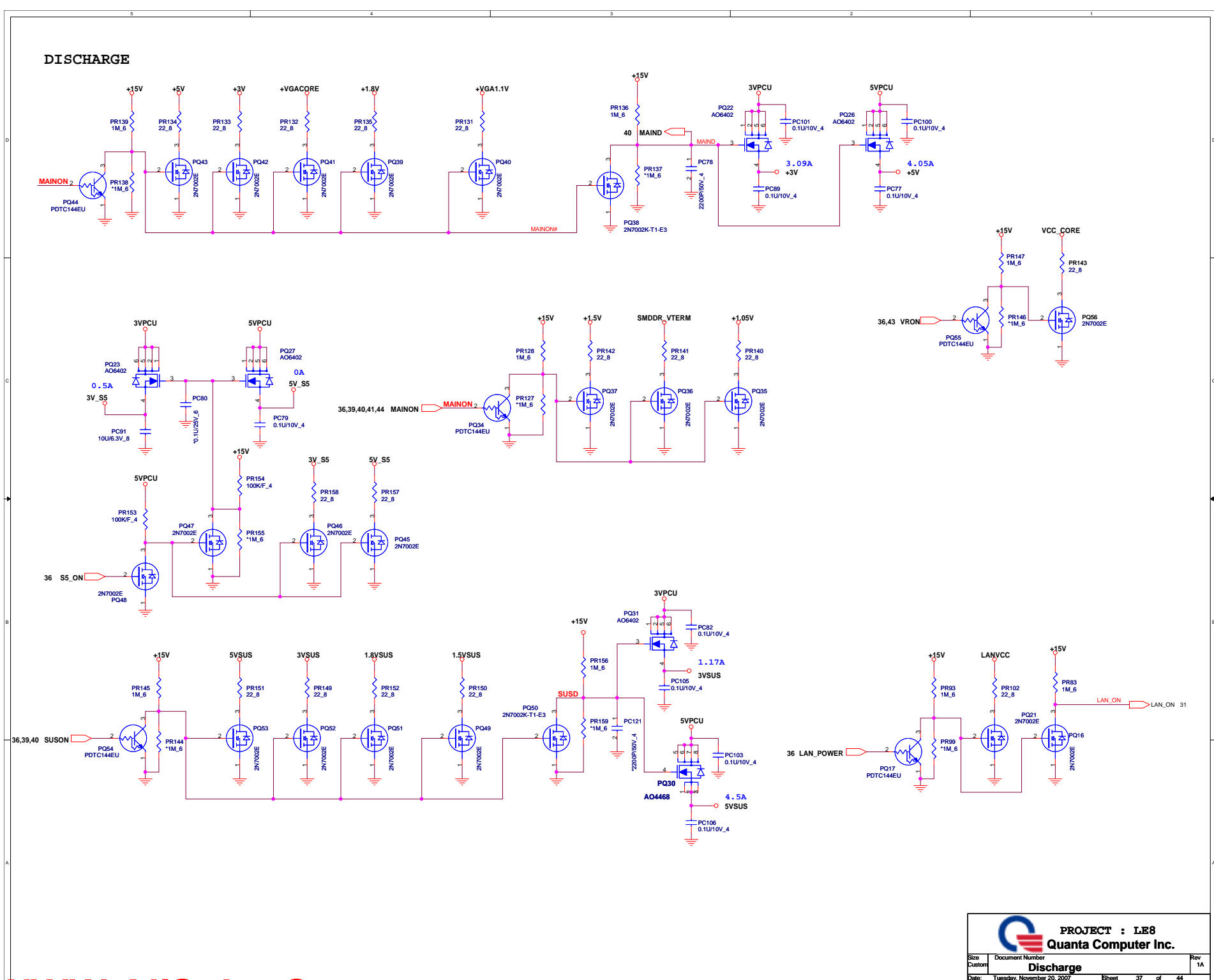


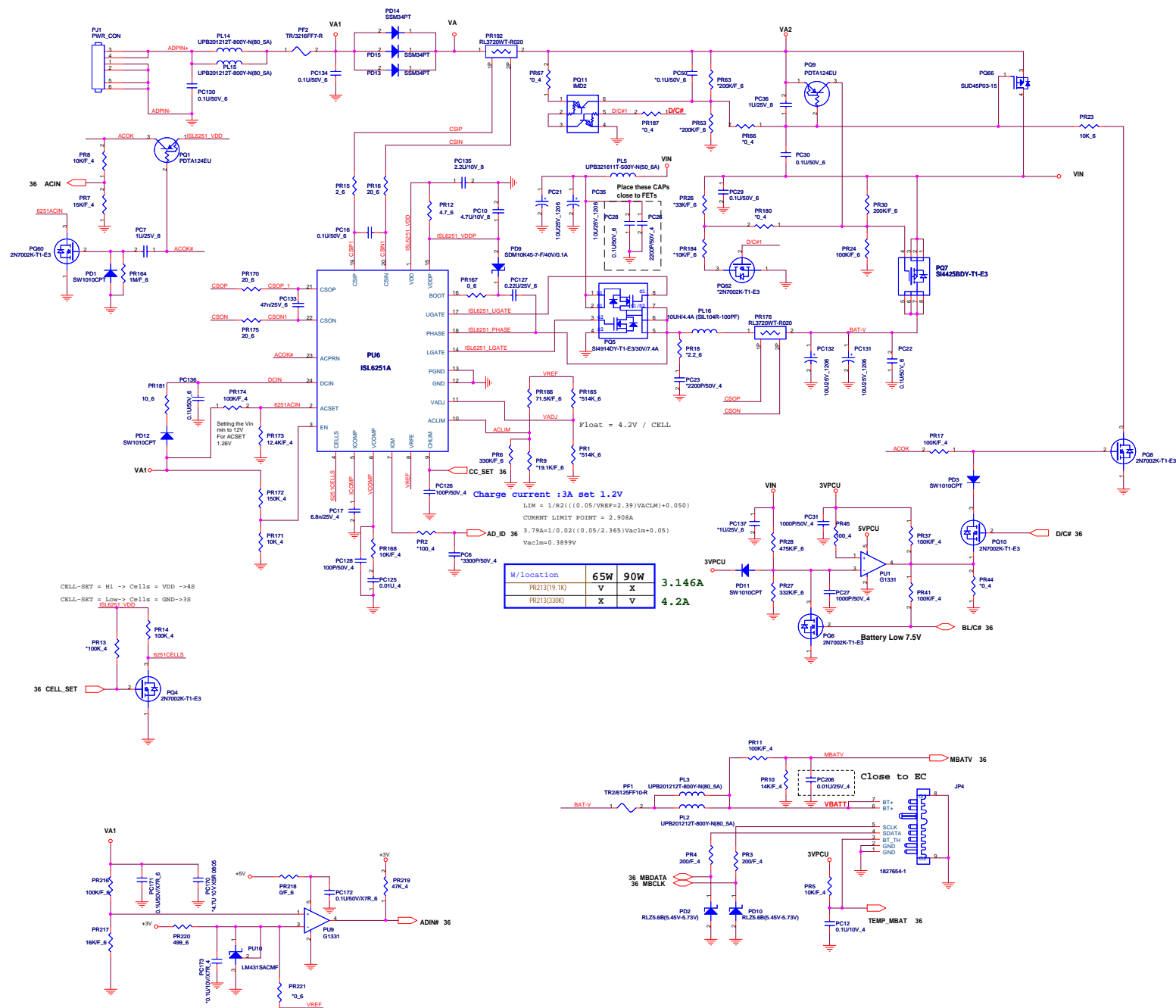
DISCHARGE

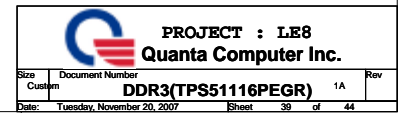
The schematic diagram illustrates the discharge section of the LE8 power supply, showing various voltage rails and their regulation stages. The main input is MAINON, which branches into several paths:

- Top Section:** MAINON branches into +15V, +5V, +3V, +VGACORE, +1.8V, +VGA1.1V, and MAINON#. Each rail is regulated by a PTC144EU (PQ44) and a 2N7002E MOSFET (PQ43, PQ42, PQ41, PQ39, PQ40).
- Right Section:** MAINON# branches into +15V, 3VPCU, and 5VPCU. The 3VPCU and 5VPCU rails are regulated by AO6402 MOSFETs (PQ22, PQ26) and 2N7002K-T1-E3 MOSFETs (PQ38).
- Bottom Section:** MAINON branches into 3VPCU, 5VPCU, +15V, +1.5V, SMDDR_VTERM, and +1.05V. The 3VPCU and 5VPCU rails are regulated by AO6402 MOSFETs (PQ23, PQ27) and 2N7002E MOSFETs (PQ47, PQ46, PQ45).
- Left Section:** MAINON branches into +15V, +5V, +3V, +1.8V, +1.5V, and +1.05V. Each rail is regulated by a PTC144EU (PQ34, PQ37, PQ36, PQ35).
- Bottom Left Section:** SUSON branches into +15V, 5VSUS, 3VSUS, 1.8VSUS, 1.5VSUS, and SUSD. Each rail is regulated by a PTC144EU (PQ54, PQ53, PQ52, PQ51, PQ49) and a 2N7002E MOSFET (PQ50).
- Bottom Right Section:** LAN_POWER branches into +15V, LANVCC, and LAN_ON. Each rail is regulated by a PTC144EU (PQ17, PQ21, PQ16) and a 2N7002E MOSFET (PQ18, PQ20, PQ15).

The diagram includes various components such as resistors (PR139, PR134, PR133, PR132, PR136, PR131, PR137, PR138, PR128, PR127, PR142, PR141, PR140, PR153, PR154, PR158, PR157, PR155, PR144, PR145, PR151, PR149, PR152, PR150, PR159, PR156, PR102, PR83, PR99, PR102, PR83, PR99), capacitors (PC91, PC80, PC79, PC101, PC100, PC89, PC77, PC106, PC103, PC105, PC121, PC82), and MOSFETs (PQ44, PQ43, PQ42, PQ41, PQ39, PQ40, PQ38, PQ22, PQ26, PQ38, PQ23, PQ27, PQ47, PQ46, PQ45, PQ54, PQ53, PQ52, PQ51, PQ49, PQ50, PQ34, PQ37, PQ36, PQ35, PQ17, PQ21, PQ16).

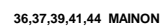


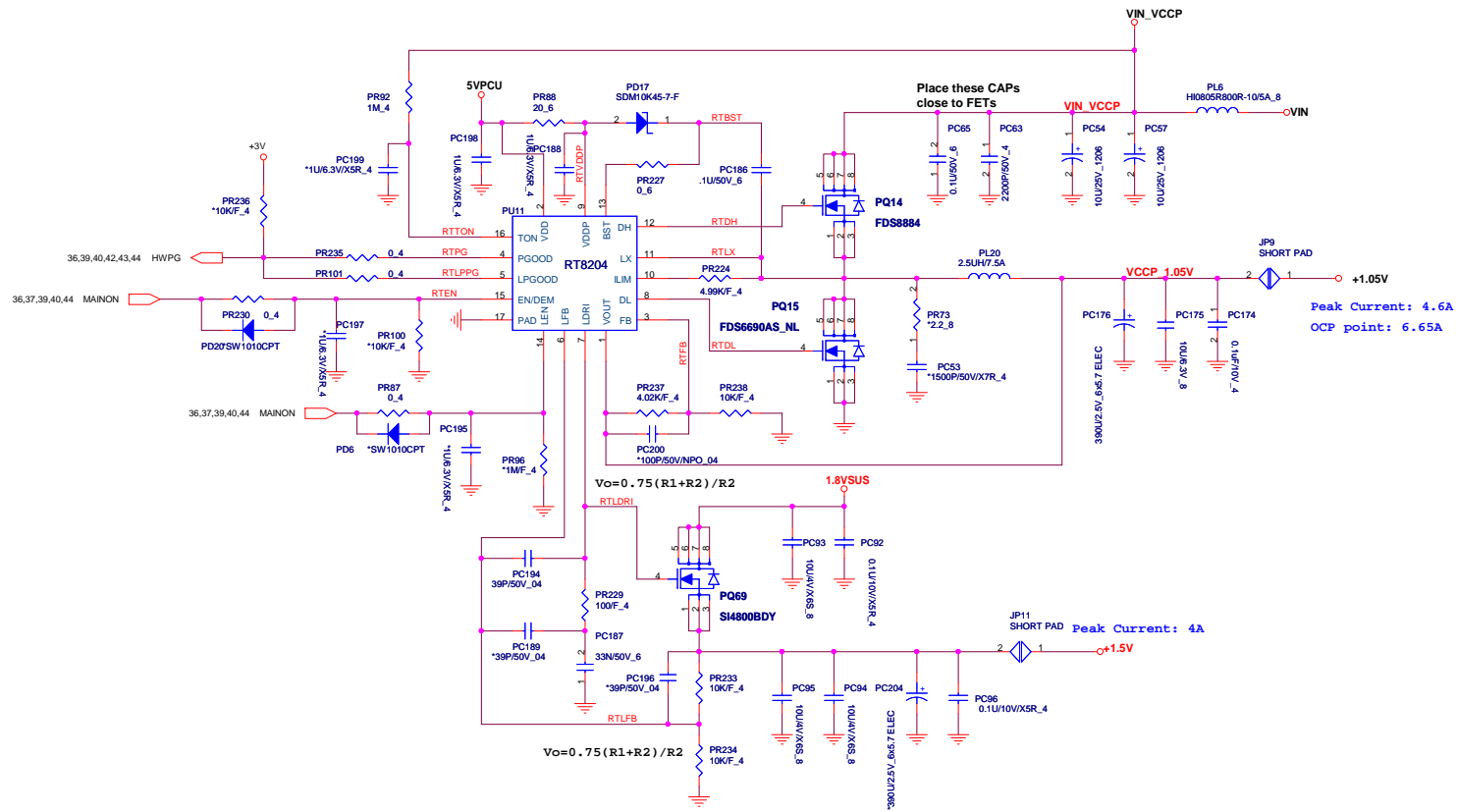




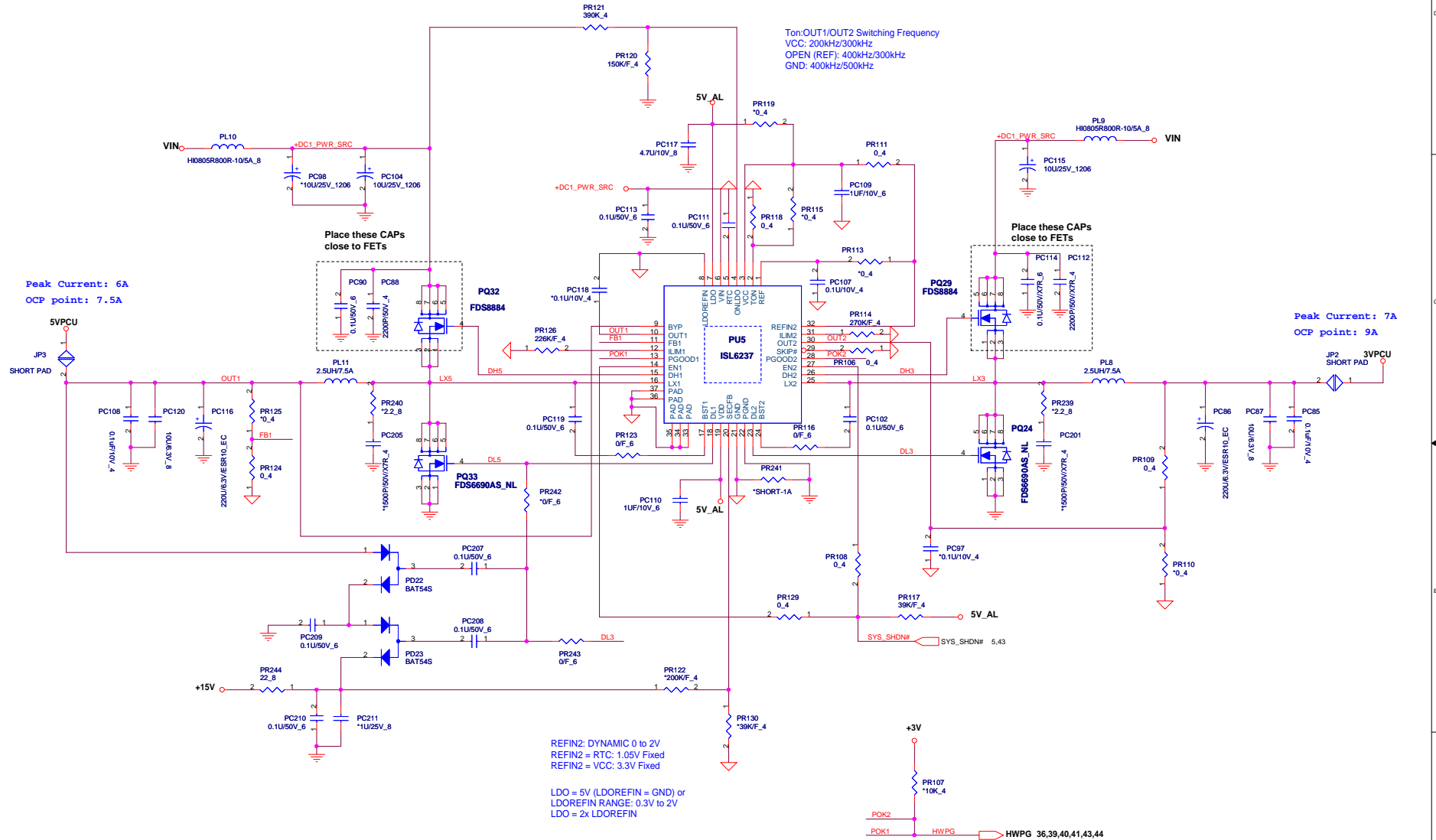


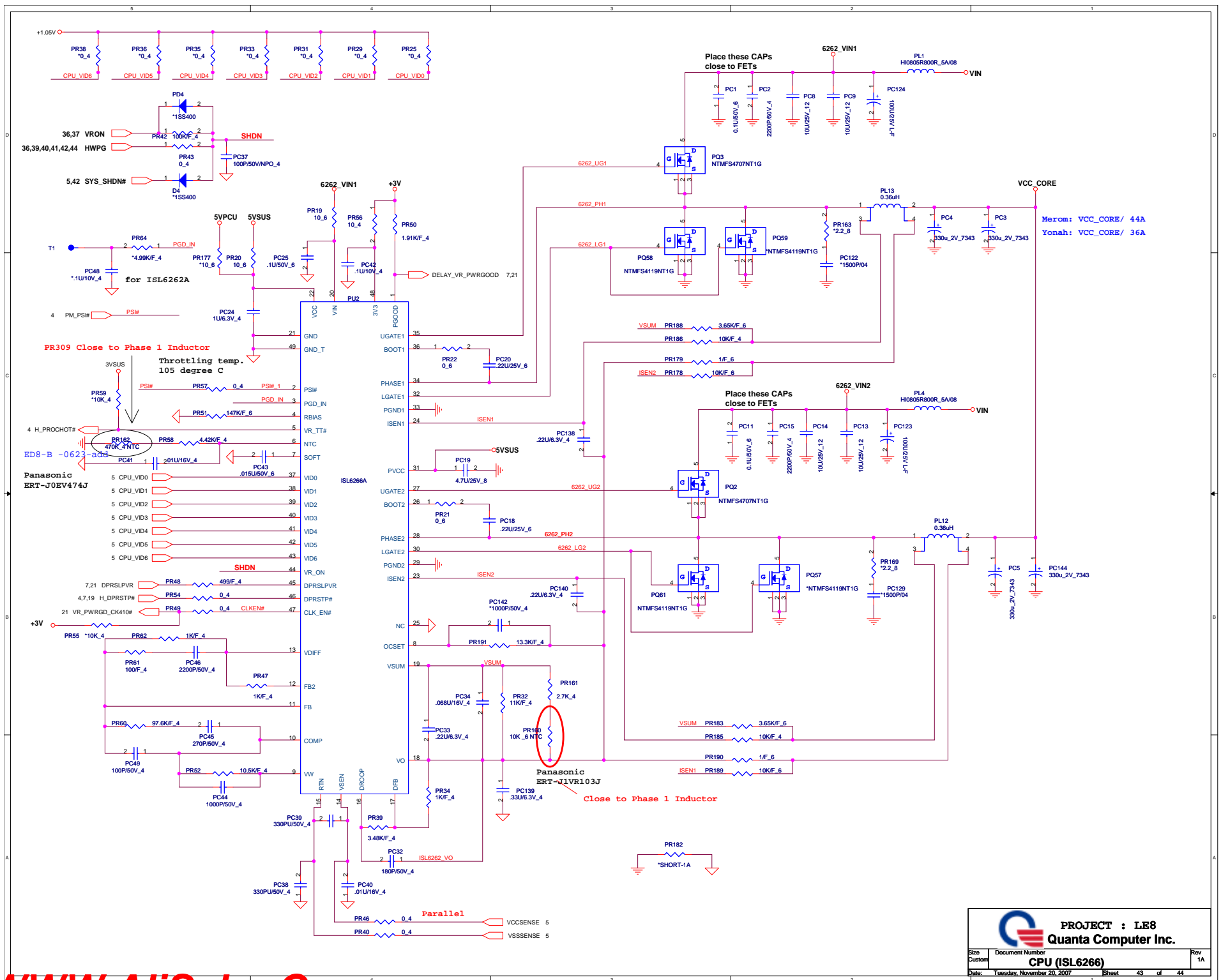
+1.8V_{RUN}



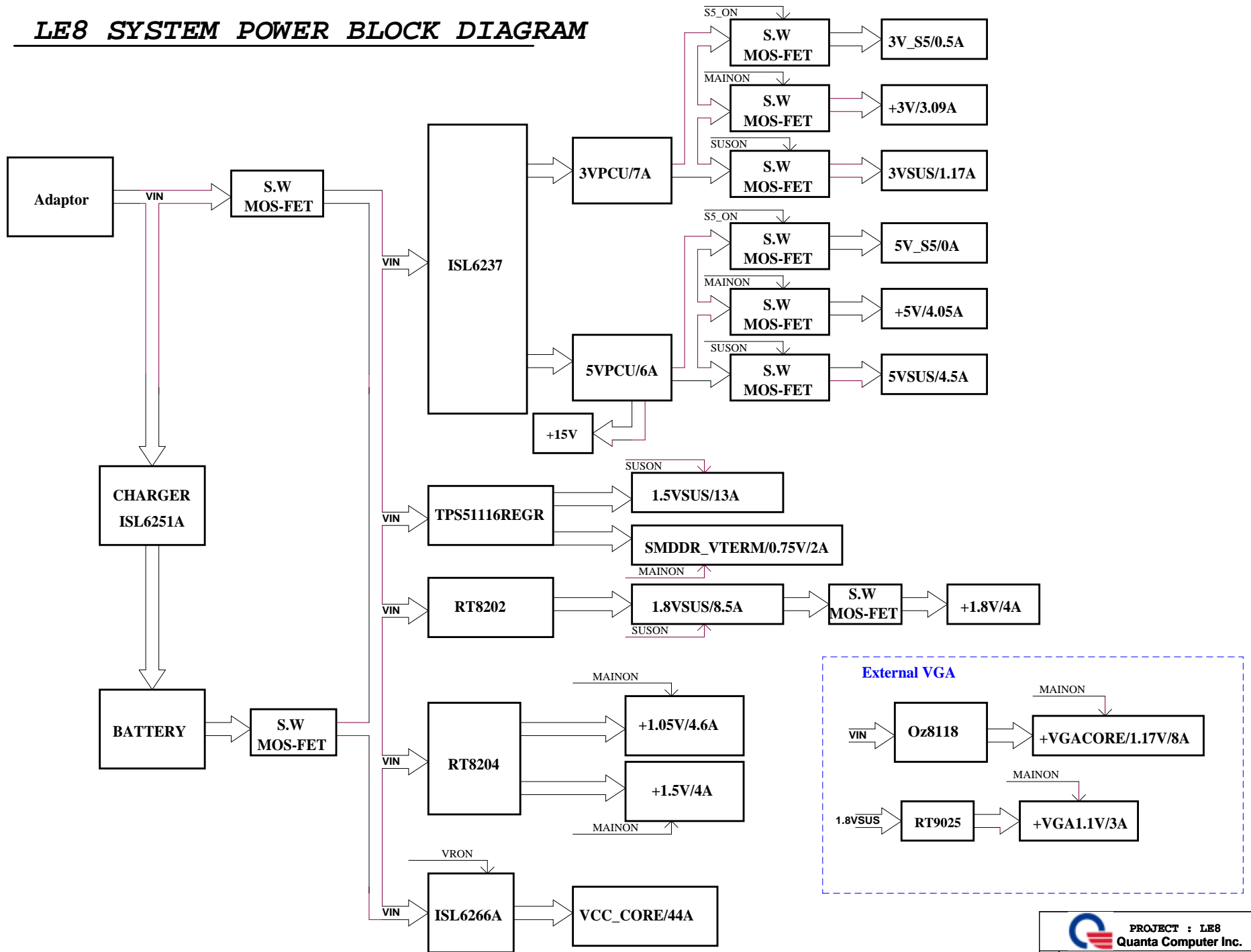


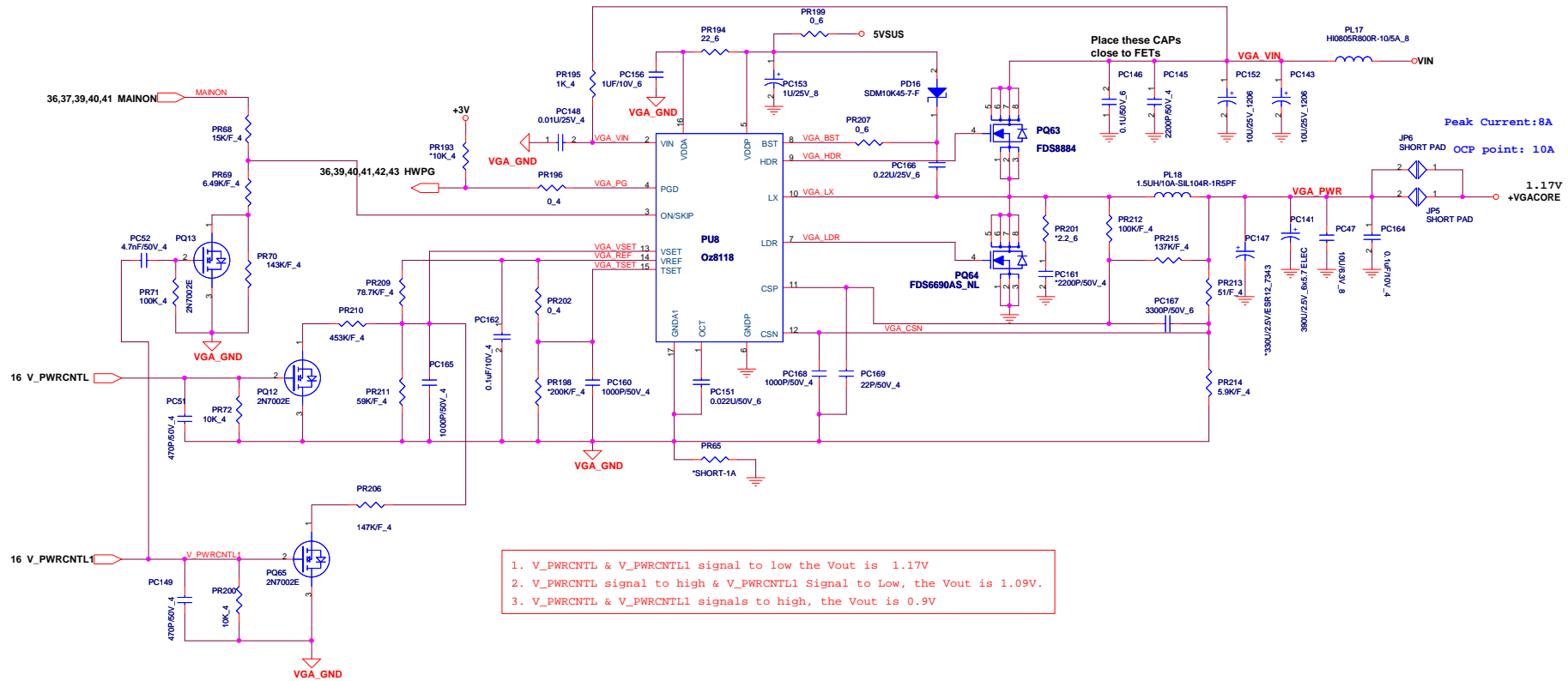
DC/DC 3VPCU/5VPCU/+15V





LE8 SYSTEM POWER BLOCK DIAGRAM





V_PWRCNTL1	V_PWRCNTL	Vout (spec)
0	0	1.17V
0	1	1.09V
1	0	0.95V
1	1	0.9V

